

Re-defining our long-term milestones

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Date	Milestone	TDR
Nov 00	PDR for CP/JEP Backplane	
Dec 00	PDR for Common Merger Module	
Dec 00	PDR for Jet/Energy-sum Module	
Jan 01	PDR for Preprocessor Module and Preprocessor ROD	
Feb 01	Completion of PRR for Preprocessor ASIC	✓
Mar 01	FDR/PRR for Preprocessor MCM	✓
Mar 01	Completion of integration tests with RoI Builder	
Dec 01	Completion of slice tests (first phase)	
Jan 02	FDR/PRR for Preprocessor Module and ROD	✓
Mar 02	FDR/PRR for Cluster Processor Module	✓
Mar 02	PDR for Timing Control Module	
Apr 02	FDR/PRR for Jet/Energy-sum Module	
Apr 02	PDR for CP/JEP Readout Driver Module	
May 02	FDR/PRR for CP/JEP Backplane	
Jun 02	FDR/PRR for Common Merger Module	
Sep 02	Completion of slice tests (second phase: mod-0 RODs and TCM)	
Nov 02	FDR/PRR for Timing Control Module	
Dec 02	FDR/PRR for CP/JEP Readout Driver Module	✓
Mar 03	Preprocessor system available for system tests	
Jun 03	Cluster Processor system available for system tests	
Jun 03	Jet/Energy-sum Processor system available for system tests	
Mar 04	Preprocessor system tests completed	✓
Jun 04	Cluster Processor system tests completed	✓
Jun 04	Jet/Energy-sum Processor system tests completed	✓
Sep 04	Preprocessor integrated with calorimeters	✓
Dec 04	Calorimeter trigger available	✓
Jun 05	<i>Full level-1 system available</i>	

Additional notes

- **PDRs** are short-term milestones, the rest are long-term.
- Combining FDRs and PRRs seems sensible, but are there too many PRRs?
- Integration with DAQ/HLT not mentioned apart from RoIB.
- Integration tests with calorimeters, and TileCal Receivers, not mentioned.
- No milestones for software.