- PPr ASIC Final Design Review:
  - > "Officially" will also need a PRR hopefully avoided by including a reviewer from Technical Coordination
  - >> Review to be held in Heidelberg in early-June 2000

>> Proposed Reviewers:	Hendrik Van Der Bij
	Paul Bright-Thomas
	Magnus Engstrom
	Viraj Perera

>> Final documentation is already on the Web

- CPM prototype Preliminary Design Review:
  - >> Couple with JEM prototype PDR to minimise travel
  - >> Review to be held at RAL (Birmingham?) on Monday 10<sup>th</sup> July 2000 (post-QMW Joint Meeting)
  - >> Agreed Reviewers: Bruce Barnett Paul Hanke Viraj Perera Uli Schaefer
  - >> Documentation to be on the Web by 23<sup>rd</sup> June

• JEM prototype – Preliminary Design Review:

>> Coupled with CPM prototype PDR

>> Review to be held at RAL on Tuesday 11<sup>th</sup> July 2000

>> Proposed Reviewers: Paul Bright-Thomas Norman Gee Paul Hanke Richard Staley

>> Documentation to be on the Web by 23<sup>rd</sup> June

- CMM prototype Preliminary Design Review:
  - >> Review will be held at RAL in August/September 2000 (dependent upon holiday absences)
  - >> Proposed Reviewers: Eric Eisenhandler Uli Schaefer Sam Silverstein Richard Staley
  - >> Documentation to be available on the Web at least two weeks in advance of the review

- CP/JEP Backplane Preliminary Design Review:
  - >> Detailed connector pinout and interconnection netlist will be available in June 2000 for CPM/JEM and CMM final designs to be specified
  - >> Detailed electrical specifications will be worked out during the summer
  - >> Review will be held in Stockholm in September 2000

>> Proposed Reviewers:	Norman Gee
	Paul Hanke
	Viraj Perera
	<b>Richard Staley</b>

- >> Documentation to be available on the Web at least two weeks in advance of the review
- PPM "Module 0" Preliminary Design Review:

>> Review will be held in Heidelberg in October 2000

>> Proposed Reviewers:	Bruce Barnett
	Paul Bright-Thomas
	Uli Schaefer
	<b>Richard Staley</b>

>> Details to be worked out with Heidelberg colleagues

- Stage 1 system tests carried out at home institutes (Heidelberg, RAL/Birmingham, Mainz/Stockholm)
- Stage 2 "Full-Slice" tests carried out at *single* institute – WHERE?
  - >> Heidelberg and RAL/Birmingham have the necessary infrastructure
  - >> In addition, the UK sites have:
    - >> expertise for 4 module designs CPM, CMM, ROD and TCM/TTCdec
    - >> very close links with ATLAS level-2 trigger
- Proposal:
  - >> Stage 2 "Full-Slice" tests *initially* at Heidelberg
  - >> Complete sufficient testing to hold full PRR
  - >> System  $\rightarrow$  RAL for L2 integration studies