

# ***Status of Reviews***

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- **5 Preliminary Design Reviews completed in 1999:**

**DSS (*and associated CMC daughter-cards*)**

**PPr ASIC ASIC**

**Serialising ASIC (*now FPGA*)**

**ROD prototype**

**TCM prototype system**

- **2 Preliminary Design Reviews completed so far in 2000:**

**PPr MCM**

**CP ASIC/FPGA**

- **2 Preliminary Design Reviews imminent:**

**CPM prototype – March 2000**

**JEM prototype (including FPGAs) – April 2000?**

- **2 Preliminary Design Reviews later this year:**

**PPM – date to be announced**

**CMM – date to be announced**

- **1 Final Design Review (PRR?) imminent:**

**PPr ASIC – March/April 2000? (*date to be confirmed*)**