Fujitsu CAN status

The majority of the ELMB code is now ported to the Fujitsu.
Some minor tweaking needs to be done, but this will require
that the whole system runs and generates CANOpen frames first.

The system as it stands is an interupt driven state machine
and it is these interupts that are causing most of the problem.

The interupts work in a single level, but I am having dificulty
with nested interupts. The problem has been isolated to the way
the ADC returns from its interupt, I have re-written the code
for this and I now think it is solved.

On another point:

It was brought to my attention via a mail-shot that Analog
Devices are about to release an 8051 derivitve microcontroller
featureing an 8 or 12 bit 7 channel ADC, RS232, SPI, I2C and
CAN interfaces. This is in a small 50 pin QFP package and is
about 1/2 the size of the MB90504 device.

Due to the problems I've had with the Fujitsu it may be worth
investigating. The 8051 is a well supported micro-core and
Analog Devices are a well respected company so I feel that
investigating this chip further would not be a futile exercise.