

# Introduction



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Electronics System Support

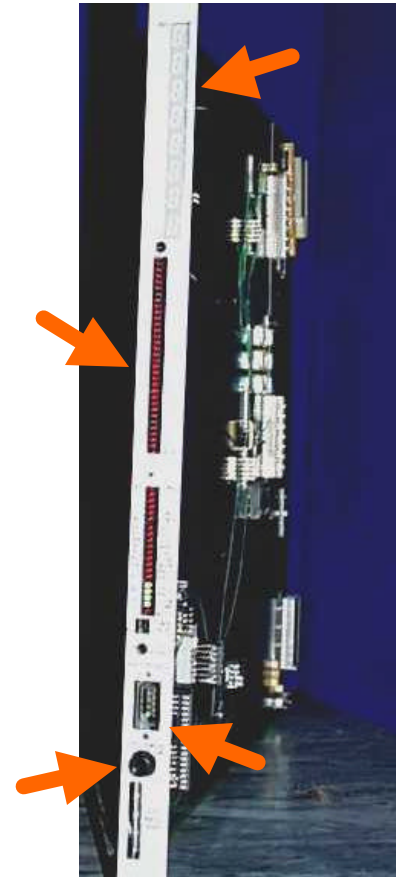
# Layout of Talk



- What is the TCM?
- Problems with the TCM
- Current Status of the TCM
- Future Options

# What is the TCM

- 9U Module
- VME Hex Display
- VME LED Display
- TTC
- CAN Bus



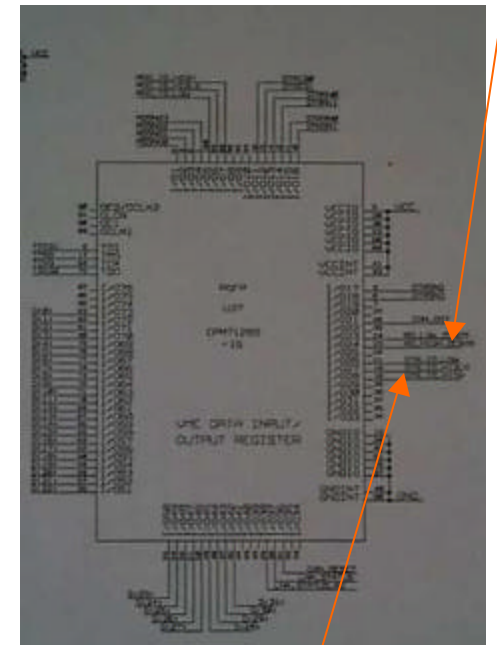
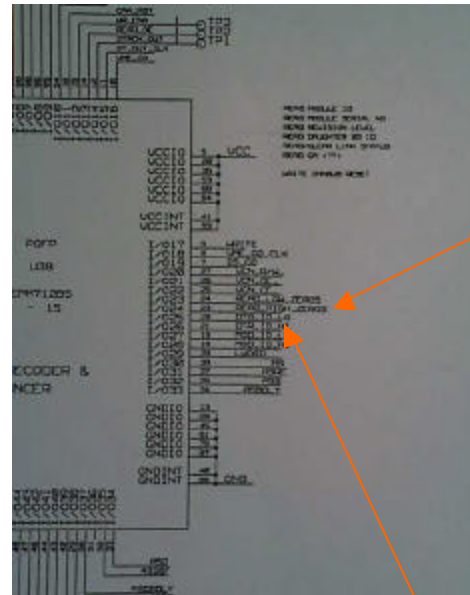
# Problems with the TCM



- Connector
  - Incorrect wiring (Write\*)
- VME Decoder
  - Net Names
  - Firmware(addrdec)
- Register Decoder
  - Net Names
  - Firmware(en2reg)
- Dual Port Memory
  - Net names
  - Timing problems
- Hex Display
  - Firmware
  - Timing problems

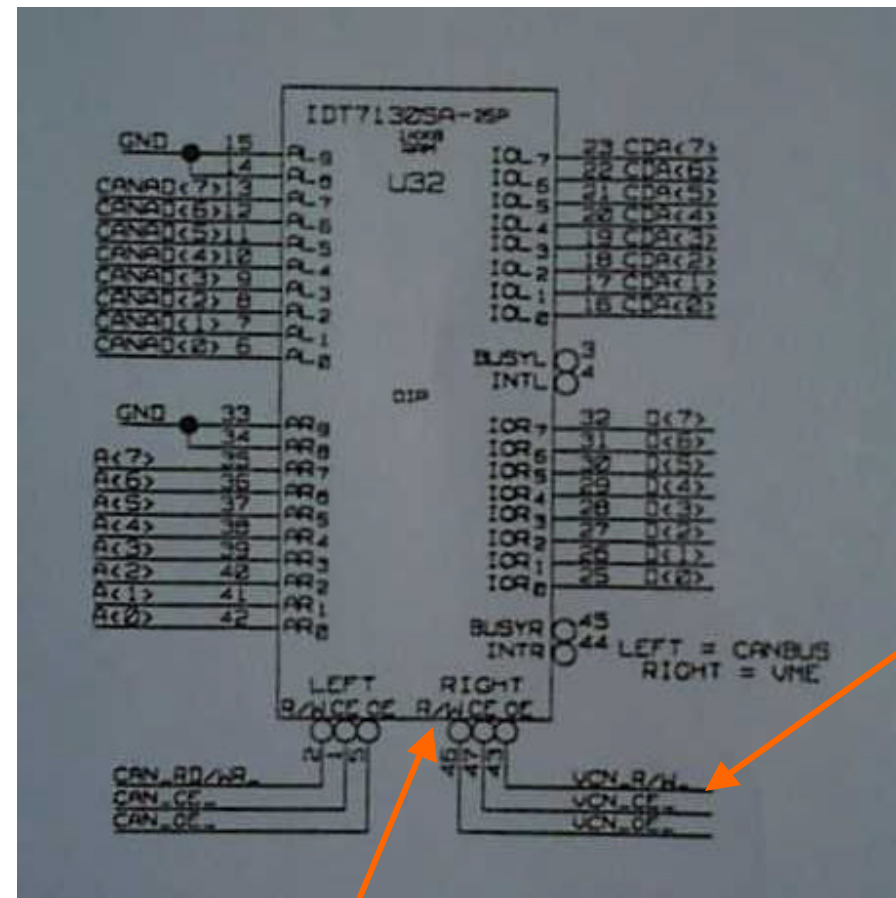
# Problem with the TCM

- Schematic
- READ\_HIGH\_ZEROS
- RD\_HIGH\_ZEROS
- DTR\_ID\_HI
- DTR\_ID\_HIGH



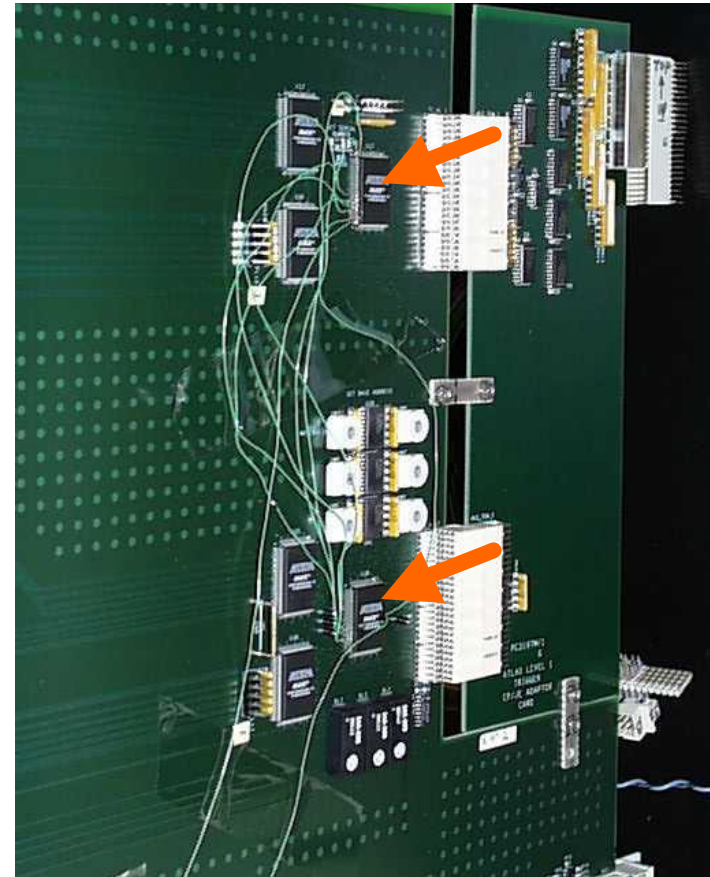
# Problems with the TCM

- Nets mixed up
  - VCN\_R/W
  - VCN\_OE\_



# Problems with the TCM

- 9 Wire Modifications
- VME Decoder
- Register Decoder



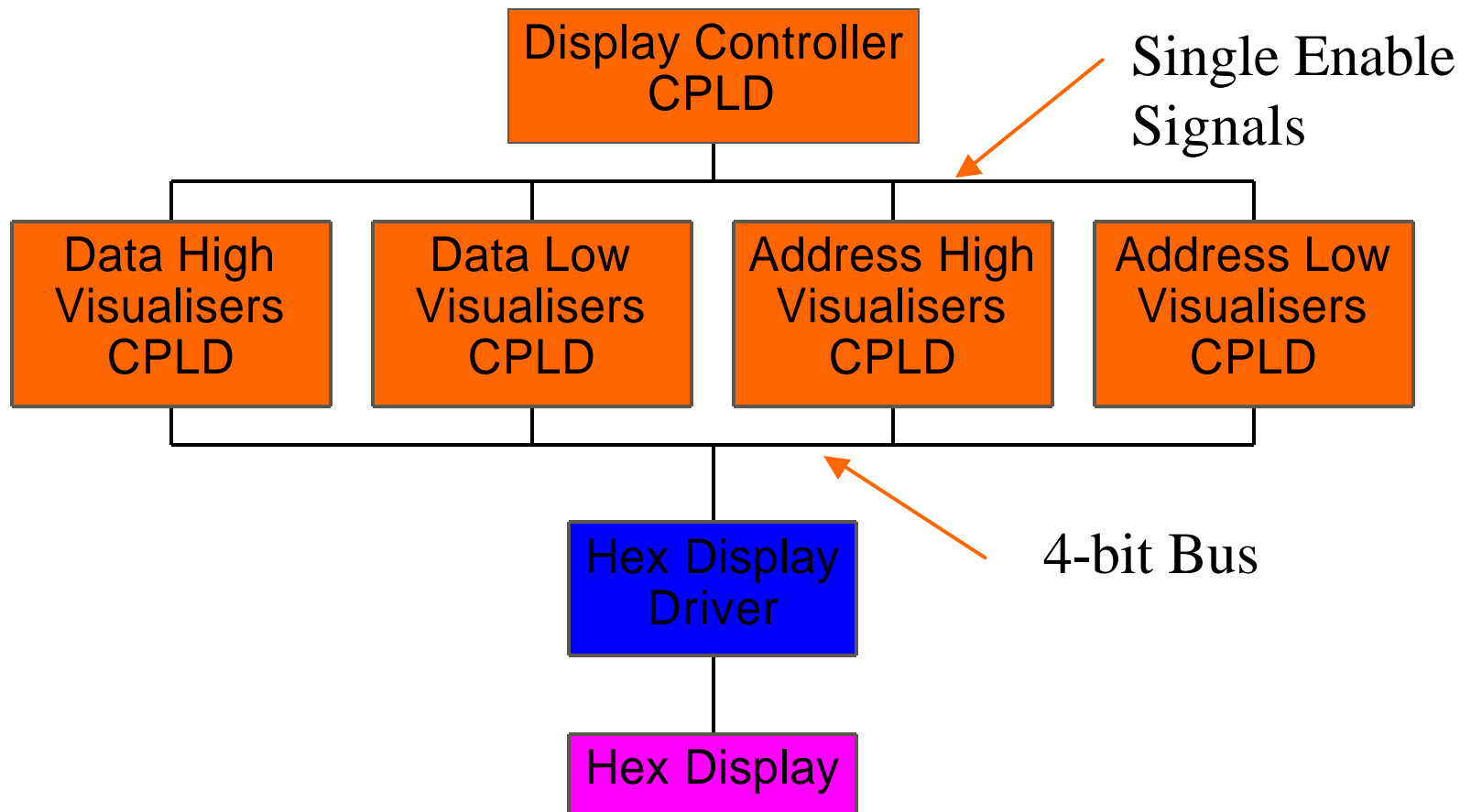
# Problems with the TCM



- Could these errors been avoided.?
- Check schematic
- Check net list
- Check again
- Is there a way of checking the net list automatically.?

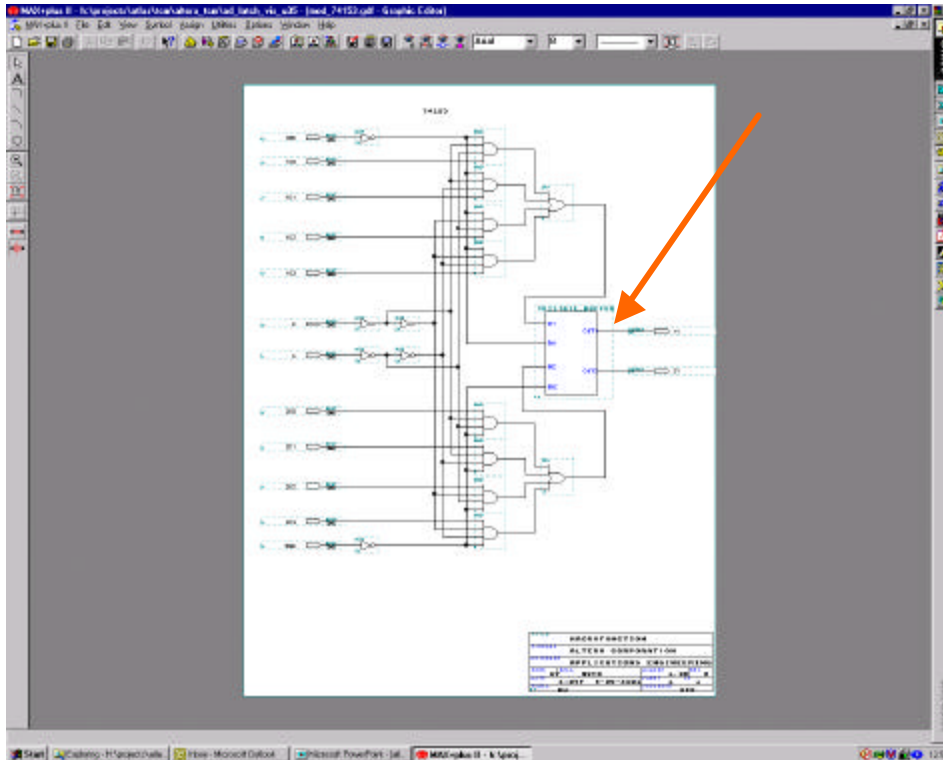


# Problems with the TCM



# Problems with the TCM

## ■ Tri\_state buffer



```
TITLE "tristate_buffer";

SUBDESIGN tristate_buffer
(
    IN1, IN2, En1, En2           :INPUT;
    OUT1, OUT2                   :BIDIR;
)

VARIABLE
    OUT1_TNODE, OUT2_TNODE      :TRI_STATE_NODE;

BEGIN

    OUT1_TNODE = TRI(IN1, En1);
    OUT1 = OUT1_TNODE;

    OUT2_TNODE = TRI(IN2, En2);
    OUT2 = OUT2_TNODE;

END;
```

# Current Status of the TCM



- Read Module ID Code "3195"
- Read Module Serial Number
- Read Daughter Serial Number
- CAN Micro controller Reset
- Can Read Link Status
- Read two other registers

# Current Status of the TCM



- Write to DPM
- Read from DPM
- "Hex display" display's Address
- "Hex display" display's Data
- LED's Display correct Data + Address

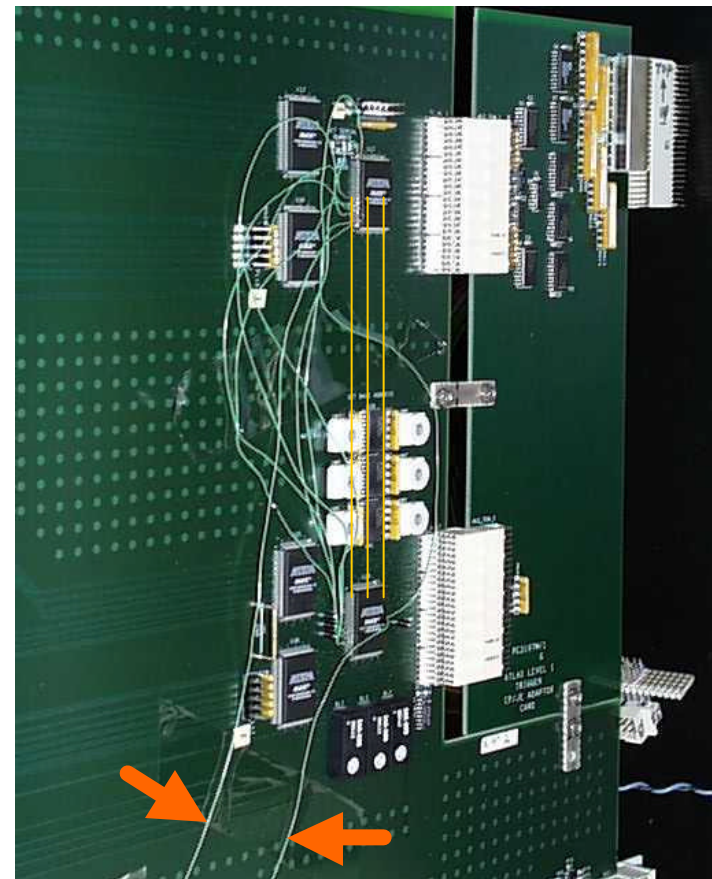
# Current Status of the TCM



- Things to be checked
  - TTC Distribution (PECL)
  - CAN bus operations

# Future Options

- Do we need to make these modifications?
- Yes, but only two of them (link status)
- Use three enable signals for address decoding and eliminate 7 wire modifications.



# Future Options



- Or, we could make the boards again taking into account the required modifications but keeping the original(ish) CPLD design.

# Future Options



## ■ Change CPLD Design

- Keep and use boards already made(7) £2534 PCB only
- New boards will have more recourses, allowing for expansion of design

## ■ Modify Boards

- Don't spend time modifying CPLD design
- Easier to debug
- Using design to full extent and as it was originally intended



# Future Options



- Recommendations
- Redesign the CPLD's
- 6 boards only need wire 2 modifications
- New boards with modifications increasing recourses if necessary

MAX+plus II - h:\projects\atlas\tem\valtera\_tcm\rd\_wr\_strobes\_mod - [rd\_wr\_strobes\_mod.tdf - Text Editor]

MAX+plus II File Edit Templates Design Utilities Options Window Help

Filedays 10

```
TITLE "Read Write Strobes";

SUBDESIGN rd_wr_strobes_mod
(
  read, write, lword      : INPUT;
  a[7..0]                 : INPUT;
  addr_ok                 : INPUT;

  mod_id_lo, mod_id_hi    : BIDIR;
  mod_id_hi2lo           : OUTPUT;

  dtr_id_lo, dtr_id_hi    : BIDIR;
  dtr_id_hi2lo           : OUTPUT;
  read_high_zeros, read_low_zeros : BIDIR;

  lnk_status              : BIDIR;
  can_rst                 : OUTPUT;

  dpnen_en_              : OUTPUT;
)
BEGIN
DEFAULTS
  dpnen_en_ = UCC;
END DEFAULTS;
mod_id_lo = TRI(0,GND);
mod_id_hi = TRI(0,GND);
dtr_id_lo = TRI(0,GND);
dtr_id_hi = TRI(0,GND);
read_high_zeros = TRI(0,GND);
read_low_zeros = TRI(0,GND);
lnk_status = TRI(0,GND);
TABLE
addr_ok, read, a[7..0]  -> dtr_id_hi2lo, mod_id_hi2lo, can_rst;
1,1,0"0000000"         -> 0,0,1;
1,1,0"0000010"         -> 0,1,0;
1,1,0"0000100"         -> 0,1,1;
1,1,0"0000110"         -> 1,0,0;
1,1,0"0001000"         -> 1,0,1;
1,0,0"0001010"         -> 1,1,0;
1,1,0"0001100"         -> 1,1,1;
0,x,0"xxxxxxxx"        -> 0,0,0;
END TABLE;
TABLE
addr_ok, a[7..0]       -> dpnen_en_;
1, 0"001xxxxx"         -> 0;
1, 0"01xxxxxx"         -> 0;
END TABLE;
END;
```

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Specifies the project name

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MAX+plus II File Edit View Node Assign Utilities Options Window Help

Ref: 0.0ns Time: 0.0ns Interval: 0.0ns

0.0ns

Name: Value:

Name	Value
read	0
addr_ok	0
a[7..0]	H 00 00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F 00 01 02 03 04 05 06
dtr_id_hi2lo	0
mod_id_hi2lo	0
can_rst	0
ad_low_zeros	Z
ad_high_zeros	Z
mod_id_lo	Z
mod_id_hi	Z
lnk_status	Z
dtr_id_lo	Z
dtr_id_hi	Z
dpmem_en_	1

