



Milestones Status (® December 2002)



<i>PBS no</i>	<i>Milestone</i>	<i>Projected date</i>	<i>Expected date</i>
10.1.2.1	Start (CP) sub-system tests	Aug 2001	Jan 2002
10.1.2.1.1.1.1	PRR for PPr ASIC	Sept 2001	? ? ? ? ? ?
10.1.2.1	Start “Slice” tests	Nov 2001	April 2002
10.1.2.1.1.1.2	PRR for PPr MCM	Nov 2001	? ? ? ? ? ?
10.1.2.1	Completion of “Slice” tests (<i>f1</i>)	March 2002	June 2002
10.1.2.1.4.8	FDR for TCM	June 2002	Sept 2002
10.1.2.1.1	FDR for PPM	July 2002	Oct 2002 ?
10.1.2.1.1.2	FDR for PPr ROD	July 2002	Oct 2002 ?
10.1.2.1.4.8	PRR for TCM	July 2002	Oct 2002
10.1.2.1.1.1	PRR for PPM	Aug 2002	Nov 2002 ?
10.1.2.1.1.2	PRR for PPr ROD	Aug 2002	Nov 2002 ?
10.1.2.1.4.9	PDR for CP/JEP ROD	Sept 2002	Sept 2002 ?
10.1.2.1.4.3	FDR for Processor Backplane (<i>PBP</i>)	Oct 2002	Oct 2002 ?
10.1.2.1.2.1	FDR for CPM	Nov 2002	Nov 2002 ?
10.1.2.1.4.3	PRR for Processor Backplane (<i>PBP</i>)	Nov 2002	Nov 2002 ?
10.1.2.1.2.1	PRR for CPM	Dec 2002	Dec 2002 ?
10.1.2.1.3.1	FDR for JEM	Dec 2002	? ? ? ? ? ?