



Processor Backplane (PBP) Status



- ***Latest information from Sam Silverstein - 16 July***
- **Design status:**
 - component placement now complete - routing progressing well (*C Bohm*)
 - net-list automatically generates “rubber-band” interconnection mesh
 - replaced trace-by-trace by manual routing, using 250 mm traces - lengthy procedure
 - pcb manufacturer will re-define final trace widths to guarantee specified impedance (*150 mm - 250 mm*)
 - **aim to complete routing (*Gerber files*) by 31 July**



Processor Backplane (PB) Status



- **Tender bids still being evaluated:**
 - one company much cheaper than others, but would not supply connector parts
 - confidence in the (more expensive) bids from the other two companies - both offer complete packages
 - collapse of telecoms market ® 2 mm connector parts liberated!
 - delivery times: 8-10 weeks from awarding contract
 - delivered tested - “bed-of-nails” connectivity tests and trace impedance measurements made
 - **contract *must* be placed by bid expiry date - 4 August**