

Dave Mills

Webpage

http://hepwww.ph.qmw.ac.uk/~dmills/fujitsu

Achieved so far

- Libraries written to control the ADC's and the RS232 interface.
- Correct operation of the libraries has verified by building a small DAQ system reading voltages and sending the digitised values to the serial port of a computer for storage.
- As of yesterday I can now receive frames that I generate.

Work in progress

- Tidying up the libraries so that other people can use them.
- Getting remote frame requests working so that we only get data when we want it.

Still to do

- Investigate CANOPEN, and write / test code for that.
- Investigate SMBus for interfacing to the temperature diodes in the FPGA's
- Test everything together...