Processor Backplane Status

Sam Silverstein, Stockholm University

- Recent developments
- Current status of crates
- Backplane errata
- Plans and outlook

Recent Developments

- Crates 3 and 4 shipped to RAL in July
- Crate 4 (incomplete) shipped to Birmingham in September
  - Tooling and power pins sent with crate
  - Some damage to front extrusion during shipping
  - Final assembly delayed (manpower shortage)

Status of processor crates

- Crate 1: At RAL. Not entirely complete (power pins for slot 1 still needed). GA0 mod?

- Crate 2: At Birmingham. Sent incomplete, but presumably completed with ground pin assemblies and long rails sent later. GA0 mod?

- Crate 3: Sent complete to RAL.

- Crate 4: At Birmingham, still incomplete. Finished crate to go to Stockholm for JEM test bench 1.

Backplane errata so far

- Geographical addr. pin GA0 grounded in slot 21.
  - ◆ Solution: remove pin
- Crate geographic addr. pins GA(6:4) <u>active low</u>
  - ◆ Other addr. Pins (and spec!) active high
  - ◆ Solution: modify module firmware
  - ◆ Messy solution…change in next revision?
- Cable shrouds randomly polarised (!)
- Shield pins on cable inputs unneeded and get in the way. Omit in next revision.

Plans and Outlook

- RAL crates should be finished
  - Send necessary parts from Birmingham?
- Crate 4 should be sent to Stockholm soon
  - JEM test bench 1
  - Characterization tests
- Should compile experiences from prototype backplanes and suggest modifications to specification before production runs