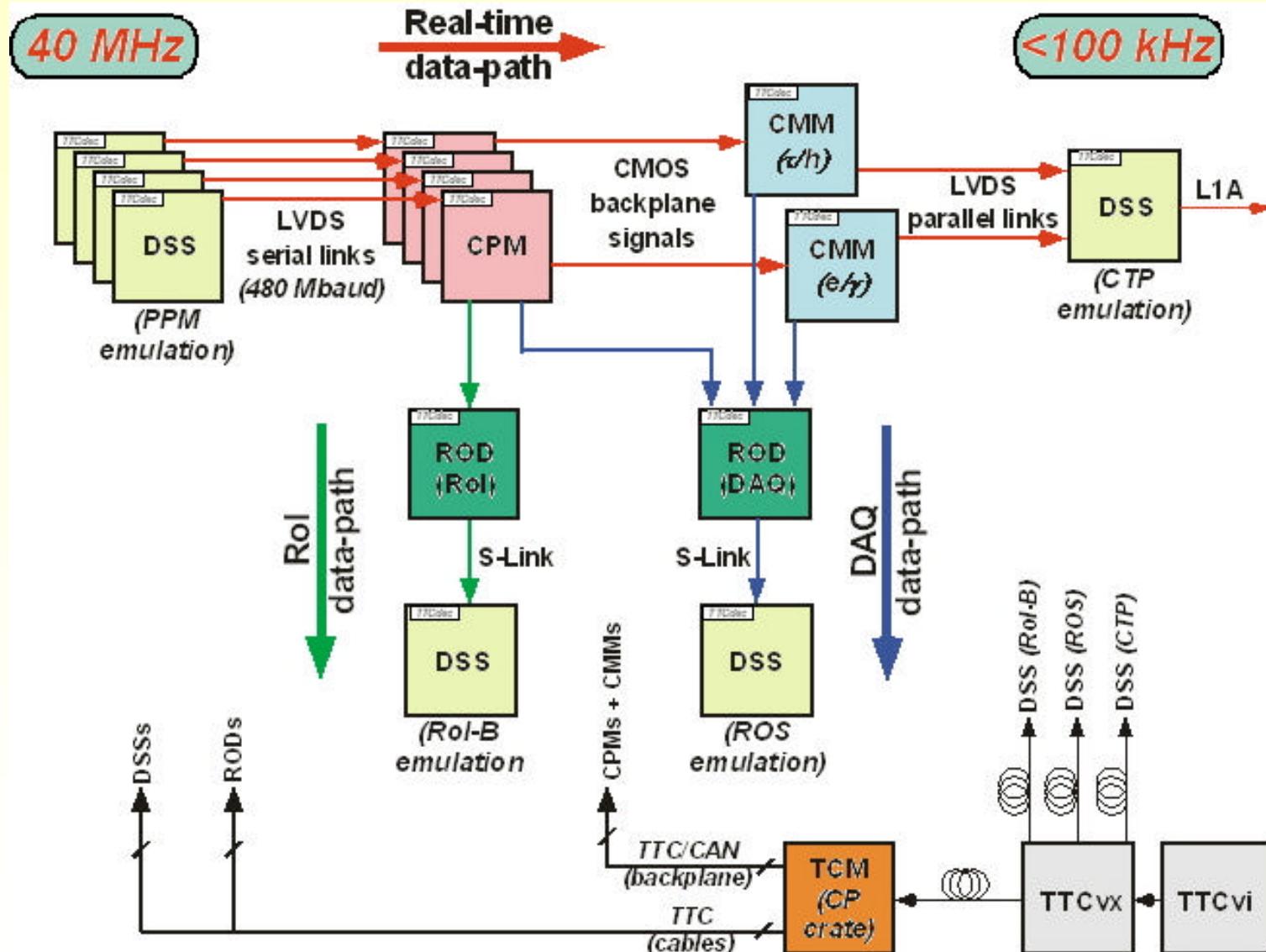
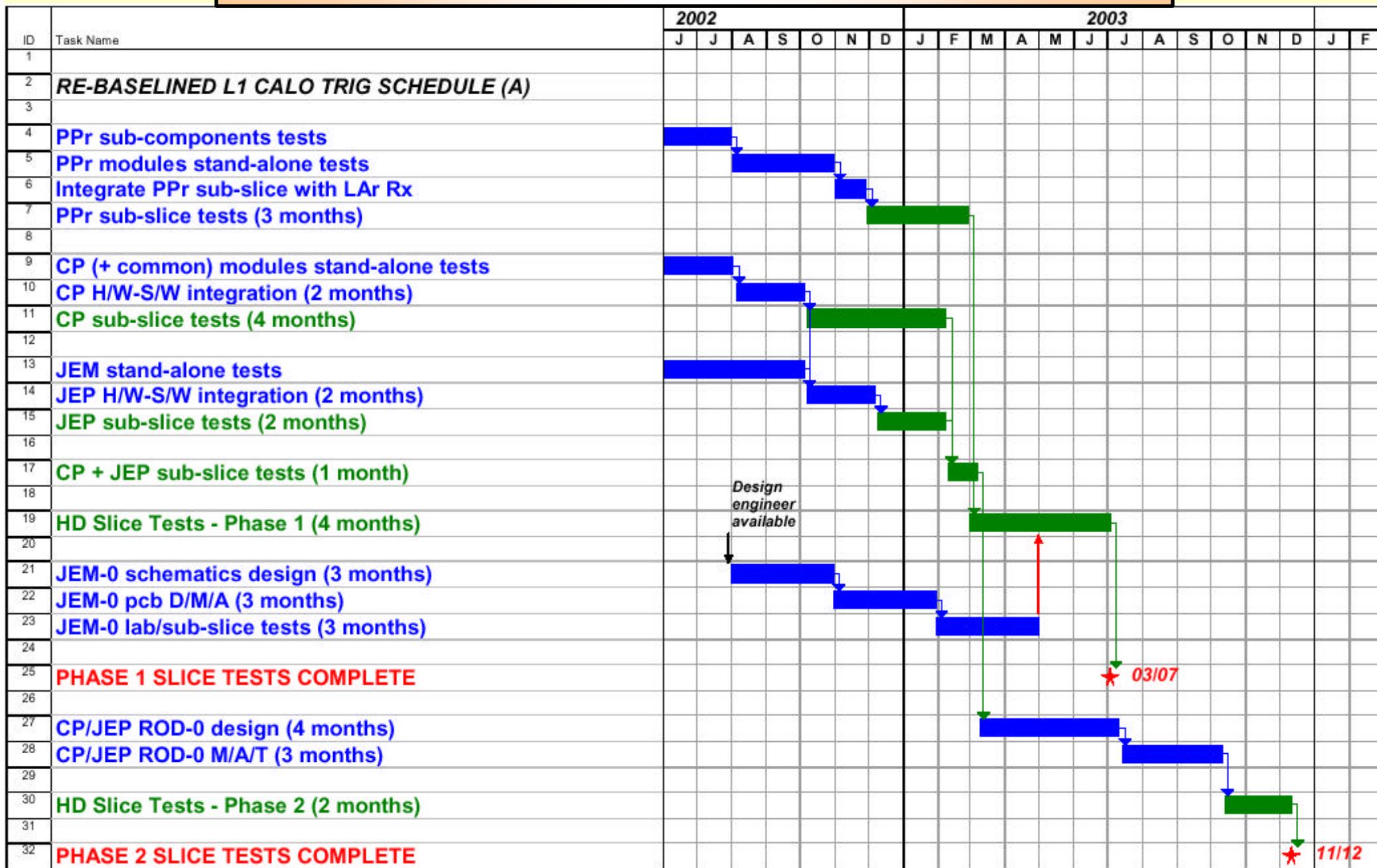




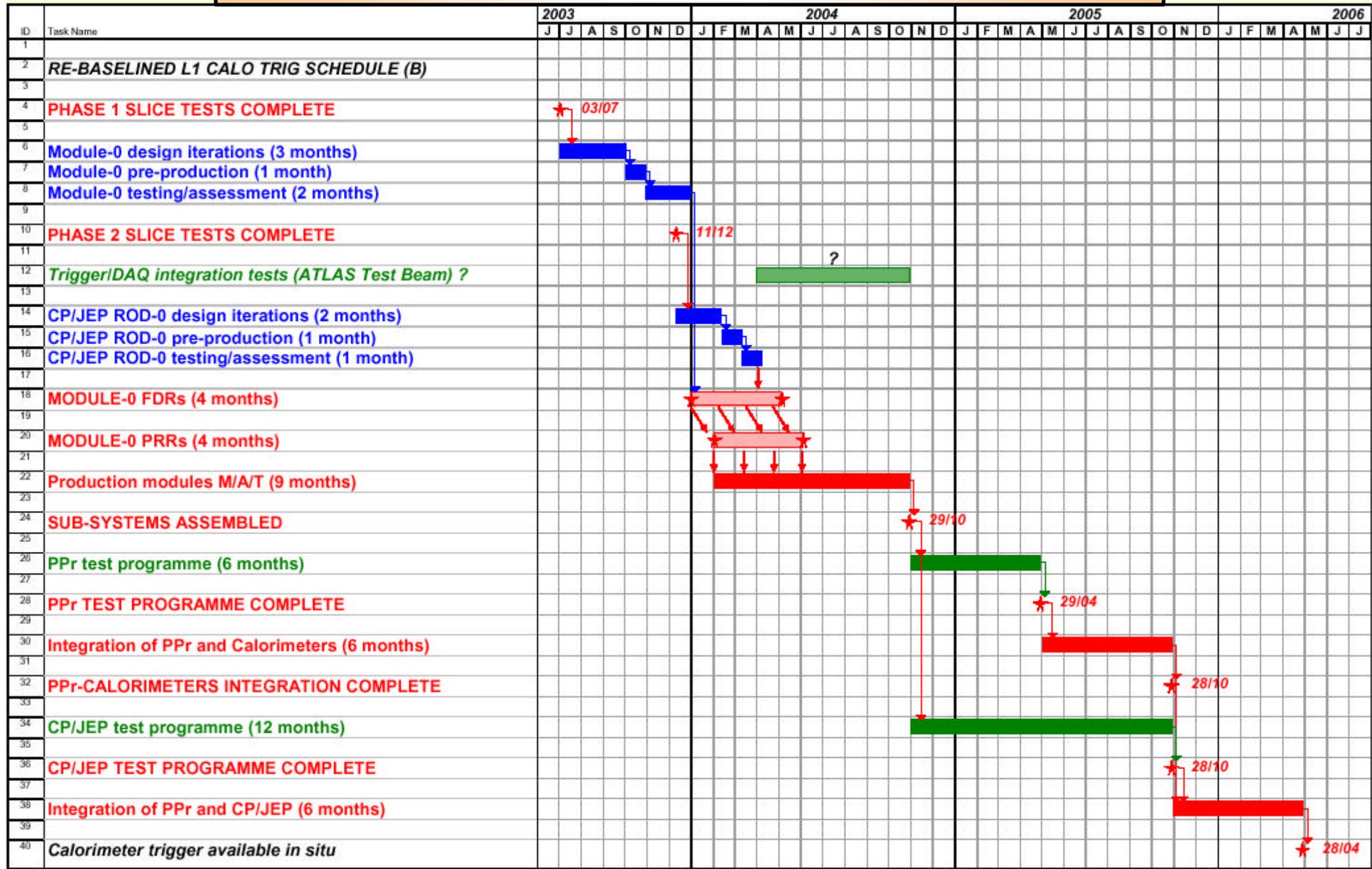
# UK Sub-System Tests – Q4 2002



# Pre-Slice Tests Schedule



# ***Post-Slice Tests Schedule***





## Revised Milestones



Milestone	ASSO Date	Revised Date	Dt
Start CP/JEP sub-system tests	Jan 2002	Oct 2002	9m
Start HD Slice Tests	Apr 2002	Mar 2003	11m
Completion of HD Slice Tests ( <i>f1</i> )	Jul 2002	Jul 2003	12m
FDR for TCM	Oct 2002	Jan 2004	15m
PRR for TCM	Nov 2002	Feb 2004	15m
PDR for CP/JEP ROD	Oct 2002	Jan 2003	3m
FDR for Processor Backplane ( <i>PB</i> )	Oct 2002	Jan 2004	15m
FDR for CPM	Nov 2002	Feb 2004	15m
PRR for Processor Backplane ( <i>PB</i> )	Nov 2002	Feb 2004	15m
PRR for CPM	Dec 2002	Mar 2004	15m
FDR for JEM	Dec 2002	Mar 2004	15m
PRR for JEM	Jan 2003	Apr 2004	15m
FDR for CMM	Jan 2003	Apr 2004	15m
PRR for CMM	Feb 2003	May 2004	15m
Completion of HD Slice Tests ( <i>f2</i> )	Apr 2003	Dec 2003	8m
FDR for CP/JEP ROD	Jun 2003	Apr 2004	10m
PRR for CP/JEP ROD	Jul 2003	May 2004	10m
Sub-systems available for System Tests	Jan 2004	Oct 2004	9m
Completion of System Tests	Dec 2004	Oct 2005	10m
Calorimeter trigger available <i>in situ</i>	May 2005	Apr 2006	11m