





Revised Milestones – August 2002



<i>Milestone</i>	<i>Revised Date</i>
Start CP/JEP sub-system tests	Oct 2002
FDR for JEM0	Oct 2002
PDR for CP/JEP ROD	Mar 2003
Start HD Slice Tests	Mar 2003
Finish HD Slice Tests – Phase 1	Jul 2003
PRR for PPr ASIC	Oct 2003
PRR for PPr MCM	Nov 2003
Finish HD Slice Tests – Phase 2	Dec 2003
FDR for Processor Backplane (PB)	Jan 2004
FDR for TCM	Jan 2004
FDR for PPM	Jan 2004
FDR for PPr ROD	Jan 2004
PRR for PPM	Feb 2004
PRR for PPr ROD	Feb 2004
FDR for CPM	Feb 2004
PRR for TCM	Feb 2004
PRR for Processor Backplane (PB)	Feb 2004
PRR for CPM	Mar 2004
FDR for JEM	Mar 2004
PRR for JEM	Apr 2004
FDR for CMM	Apr 2004
FDR for CP/JEP ROD	Apr 2004
PRR for CMM	May 2004
PRR for CP/JEP ROD	May 2004
Sub-systems assembled for System Tests	Oct 2004
Finish PPr test programme	Apr 2005
Finish CP/JEP test programmes	Oct 2005
Finish PPr-Calorimeters integration	Oct 2005
Calorimeter trigger available in situ	Apr 2006