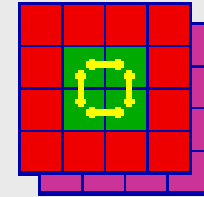




ATLAS Level-1 Calorimeter Trigger



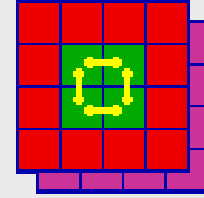
CP/JEP Rod Update

In the Loop

Bruce M. Barnett



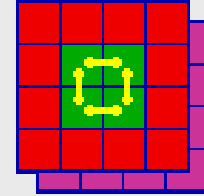
Overview



- Software Status (3)
- Firmware Status
- Plans



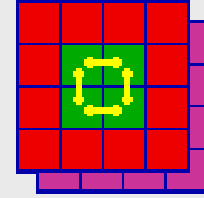
Software Status (1/3)



- Urgent need to make progress on “real” slice-test software
 - Update test software: (Done)
 - Now uses CpRod, Ttcvi and Dss classes from the module services package(s).
 - CpRod, Ttcvi, Dss and Cbd classes are now functional and use HDMC hardware access layer and parts files.
 - Looper restructured to use module-service “DaqInterface” which these classes support.
 - Initial move to CMT
 - Include CERN-VME driver support in HDMC



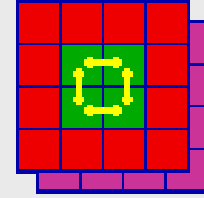
Software Status (2/3)



- Update test software: (To Do)
 - Incorporate use of L1Calo Database interface:
 - Use to setup specific registers on “load”.
 - Change arguments of most module-service methods to directly access database object (rather than store a local copy.)
 - Reimplement (my) local database to use L1Calo db-interface.
 - Use a vector-file-reader (‘generator’) to access Steve’s simulation replacement for W. Stokes’ code.
 - Support issues:
 - Tidy tcm and cmm frameworks.
 - Check Dss Variants (identify un-implemented methods.)



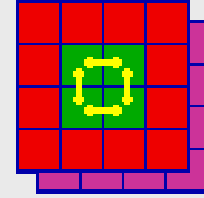
Software Status (3/3)



- Update test software: (To Do)
 - Restructure to allow use from run-control:
 - Separate Looper, Readout, “dss-kicker”, and basic setup/control functionality.
 - Allow for ‘kicker’ fork and control via shared-memory (implemented as an HDMC pseudo-part.)
 - Complete module-services move to CMT.
 - Update HDMC repository. L1Calo repository
 - Docs... (aaarrgh).



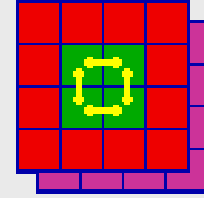
Firmware Status



- Not much new, but:
 - No recurrence of flow control problems.
 - Still need soak tests with varied vectors.
 - Old (non critical) problem re-identified:
 - Steve tumbled over a stuck bit which I had long ago noted and allowed for ... but feedback to James had been delayed by more dynamic/urgent problems. (should readdress this problem, or change docs.)
 - The test s/w works again
 - So I can re-enter this test loop.



Plans



- S/W:
 - Address priority items:
 - Having to do with module-service interface.
 - Incorporation with run-control.
- Firmware:
 - Re-evaluate status and test plan.
 - Multi-slice operation, RoI operation.
 - Drown-tests.
 - New Variants?
 - (Dss L1A emulator, JEP Rod?)