



## Firmware (ID responsibility)

- What Firmware
- Where are we
- Who is doing what
- When
- Where to get the bit files
- Problem Reports



## Firmware (ID responsibility)

- What Firmware

- Existing code needing support or testing
  - CPCHIP ('normal mode')
  - CP CHIP ('scan path')
  - Serialiser
  - CMM
    - Crate
    - System
    - VME
    - I2C
    - Flash controller
  - CPROD CPM slices and CPM RoIs: written and being debugged



## Firmware (ID responsibility)

- New Firmware required:
  - (a) Diagnostic Modules
    - DSS - wraparound at end of data.
    - DSS L1A Generator
    - DSS support for GIO CMC card
  - (b) Cluster system
    - CPROD Cluster CMM slices



## Firmware (ID responsibility)

- New Firmware required:
  - (c) Jet System
    - CPROD JEM slices
    - CPROD JEM RoIs
    - CPROD Jet CMM slices
    - CPROD Jet CMM RoI
    - Jet CMM (crate and system)
  - (d) Energy System
    - Energy CMM - (not a UK responsibility)
    - CPROD Energy CMM slices
    - CPROD Energy CMM RoI



## Firmware (ID responsibility)

- Gantt Chart
  - Where are we
  - Who is doing what
  - When



## Firmware (ID responsibility)

- How to get it
  - <http://www.te.rl.ac.uk/esdg/atlas-flt/>
    - CP CHIP
    - Serialiser
    - CMM (nothing linked yet)
    - CPROD
    - DSS



## Firmware (ID responsibility)

- Problem Reports

- To keep a track of all problems and fixes with modules and firmware
- Fill and email it to me (Viraj) for ID modules and firmware
- Completed forms to be made available on the web soon
  - Require password protection to put in place
- Form at <http://www.te.rl.ac.uk/esdg/atlas-flt/>
  - QA
    - Problem Report.doc