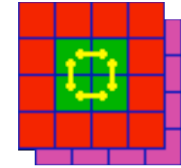




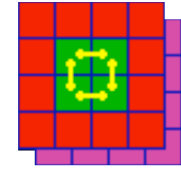
Simulation and other bits...



- o Not much to report!
 - o Some simulation progress
 - o Simulation plans
 - o Comments on JEM



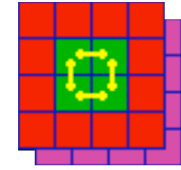
Simulation progress (limited)



- o Updated to full ANSI/ISO compliance
 - o Using new compiler at CERN
- o Now uses CMT check target mechanism
 - o Checks simulation outputs against known data



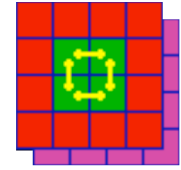
Simulation Plans (plentiful)



- General:
 - Work on DSS L1A generation
 - Test vector extension for multiple step runs
 - Multislice operation (for CPM and others)
- Module specific:
 - Extra functionality for CPM for hardware tests
 - New generators, playback/spy capabilities
 - Check ROD variant simulations against hardware
 - Work with Norman on CMM
 - JEM integration



Comments on JEM



- o Simulation still needs lots of work
 - o Though I haven't had any contact recently
- o Comments from JEM review
 - o Memory map will probably look similar
 - o Do not intend to run JEM 0.1 and JEM 1.0 at the same time