



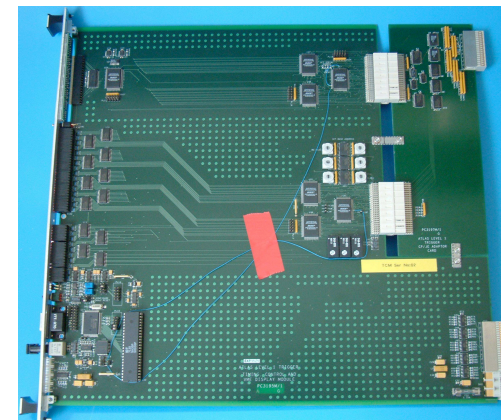
Progress/Status April 2003

- **'Bits and Pieces'**
 - TCM -> ALC-VME
 - TTCrxDec
 - DSS
 - GIO
 - G-Link CMCs
 - RGTM



Progress/Status April 2003

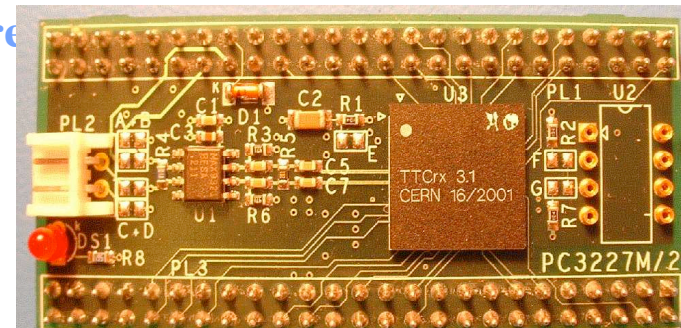
- **TCM ALC-VME**
 - The pre-processor and the final ROD crate require VME compatible adaptor link card (ALC)
 - A draft specification was released (17th March 2003) for discussion.
 - Schematic has been entered but awaiting for spec approval (mainly pin-out of J0 for differential TTC signals and CAN bus) to complete the job.
 - A module is required by June 2003!





Progress/Status April 2003

- **TTCrxDec Cards with Rad-hard TTCrx chip**
 - 35 modules have been manufactured, some are in use some require testing.
 - New TTCrx decoder cards for the prototype ROD modules have been designed and sent for PCB manufacture
 - Double-pole LEMO 00 connector
 - PL1 and PL3 connectors swapped to match the ROD
 - Red colour PCB (Use on ROD only)
 - 10 PCBs arrived at RAL yesterday (16th)
 - 10 TTCrx chips have been ordered





Progress/Status April 2003

- **DSS Modules**

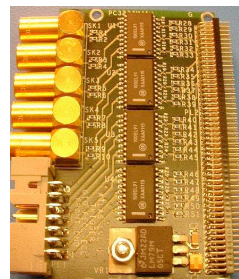
- Eight new DSS modules were ordered on the 14th of March.
- They are being assembled now, due back at RAL end of April.
- Out of the first batch of ten, two still require attention (error in one memory block).
- The front panels of all the DSS modules will be modified to take the TTCrx signal via a double pole Lemo 00.



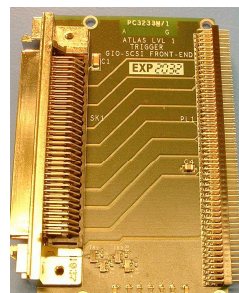
Progress/Status April 2003

- **GIO card version 2**
 - CMC cards for DSS
 - Two modules (back-end) successfully tested with the two front-end modules: LVDS-SCSI and ECL.
 - The remaining six cards have been sent for assembly (due end of April)

Front-end ECL



Front-end LVDS-SCSI

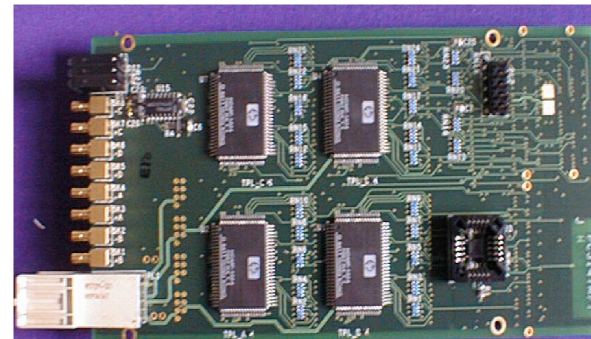


Back-end XC2V250



Progress/Status April 2003

- **G-Link receiver and transmitter cards**
 - Minor modifications to the existing cards
 - Lemo 00 connectors
 - Link locked indicators
 - Test points
 - JTAG EPROMs
 - Layout underway now after up-rev problems
 - Not a complete re-layout





Progress/Status April 2003

- **Rear G-Link Transition Module (RGTM) for Heidelberg**
 - Pre-processor to ROD link
 - VME Rear Transition Module (3U x 60mm)
 - Data, clock, etc taken from P2
 - Schematic completed
 - Queued in DO for layout (next after G-link cards)

