



17th April 2003
RodCrateDaq Meeting
26-Mar-2003



**Brief summary. For details see
slides from Rod Crate Daq meeting:
<http://agenda.cern.ch/age?a03444>**



C .N .P .Gee
Rutherford Appleton Laboratory



M Joos: Hardware



- **CERN will support diskless booting of concurrent CPUs**
- **TTC support re-organised after retirement of B Taylor. M. Joos is the contact for the VME modules.**
- **ROD Busy Module drivers and library are available, docs in EDMS.**
- **Confirmed that there is no Atlas-standard Rod Crate TTC distribution scheme.**
- **Atlas needs ~1000 Slinks. M Joos would provide central support for S-link hardware, but needs request. Dig will be asked.**



M Joos et al: Software



- **We must read & comment on Local Trigger processor (LTP) proposal (now we have)!!**
- **Rod Crate Daq software is organizationally part of Dataflow and stored in their cvs repository.**
- **There are CERN VME and TTCvi libraries. Also PCI drivers that poke into PCI addresses from user space.**
- **Ralf Spiwoks could start CTPD integration tests from July 2003. He has a concurrent CPU. CTPD requires a CERN 9U crate.**
- **G Ionescu (LAr) has a Calibration Test Bench (software). They use a command execution strategy c.f. our state-based model.**



B. di Girolomo: Calibration



Atlas Calibration & Monitoring requirements:

- **Atlas has 106 crates with 1020 RODs plus other ROD-like crates.**
- **Typical calibration involves looping on some quantities and measuring others, and manipulating the electronics.**
- **Tilecal Cesium source is an exception.**
- **Talks to DIG on calibration are linked to DIG web page.**
- **Input is invited from Level-1 on L1 requirements.**



RCD Emulation and Data Monitoring



- **If you haven't a ROD (e.g. CTPD), software pulls data over VME from your module into a Software Robin, then transmits it over an S-Link from the SBC.**
- **Transmission can also be over Ethernet.**
- **Ralf can sustain 20kHz of 80 byte data (needs "yield patch")**
- **Data monitoring framework is being developed for RCD.**
- **RCD could be ready ~ Middle 2003.**