

9U ROD

- **Time Scales (plan)**
 - Release Specification draft 0.0 by 23rd May 2003
 - Brainstorming session first week of June
 - Release draft 1.0 by 18th June
 - PDR first week of July
 - Design to DO by middle of October
 - PCB Manufacture and assembly by end November
 - Module for testing in January 2004
- **PDR to Module in hand for testing ~ 6 months!**
 - Summer and Christmas holidays

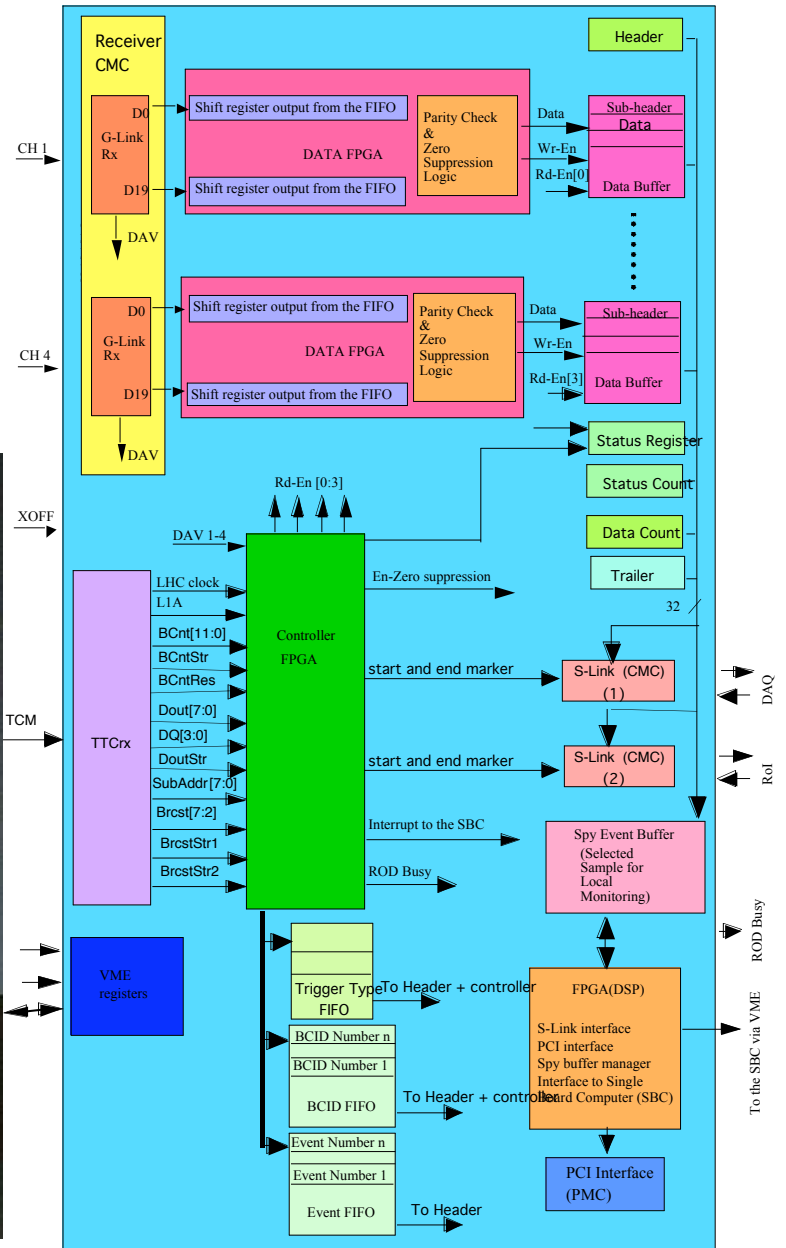
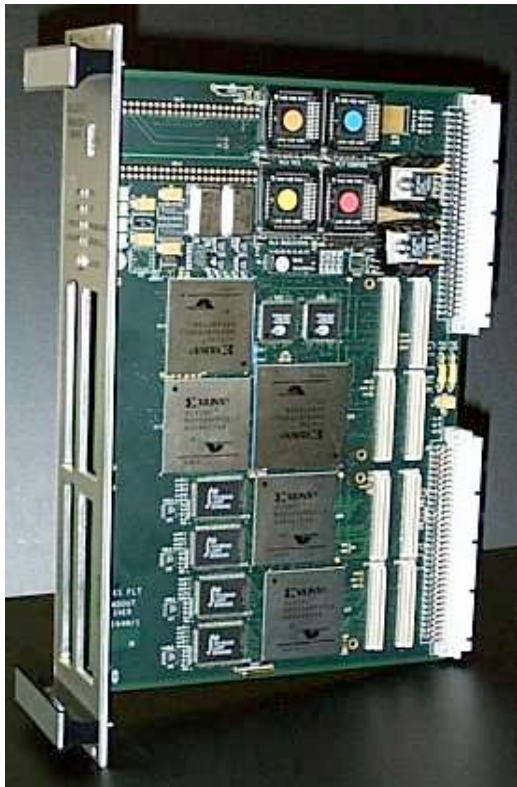
9U ROD

- **Strategy**
 - Use much as possible from 6U Prototype ROD
 - e.g. Firmware
 - Avoid unnecessary complications
 - e.g. Plug-in PMC processor modules
 - Untested in 6U prototype ROD
 - Future proofing!
 - Something not available on day one (January 2004) can be designed in later (e.g compression)
 - Use ‘Modular Design’ techniques to reduce design time
 - James, Ian and Panagiotis to work on the design

9U ROD

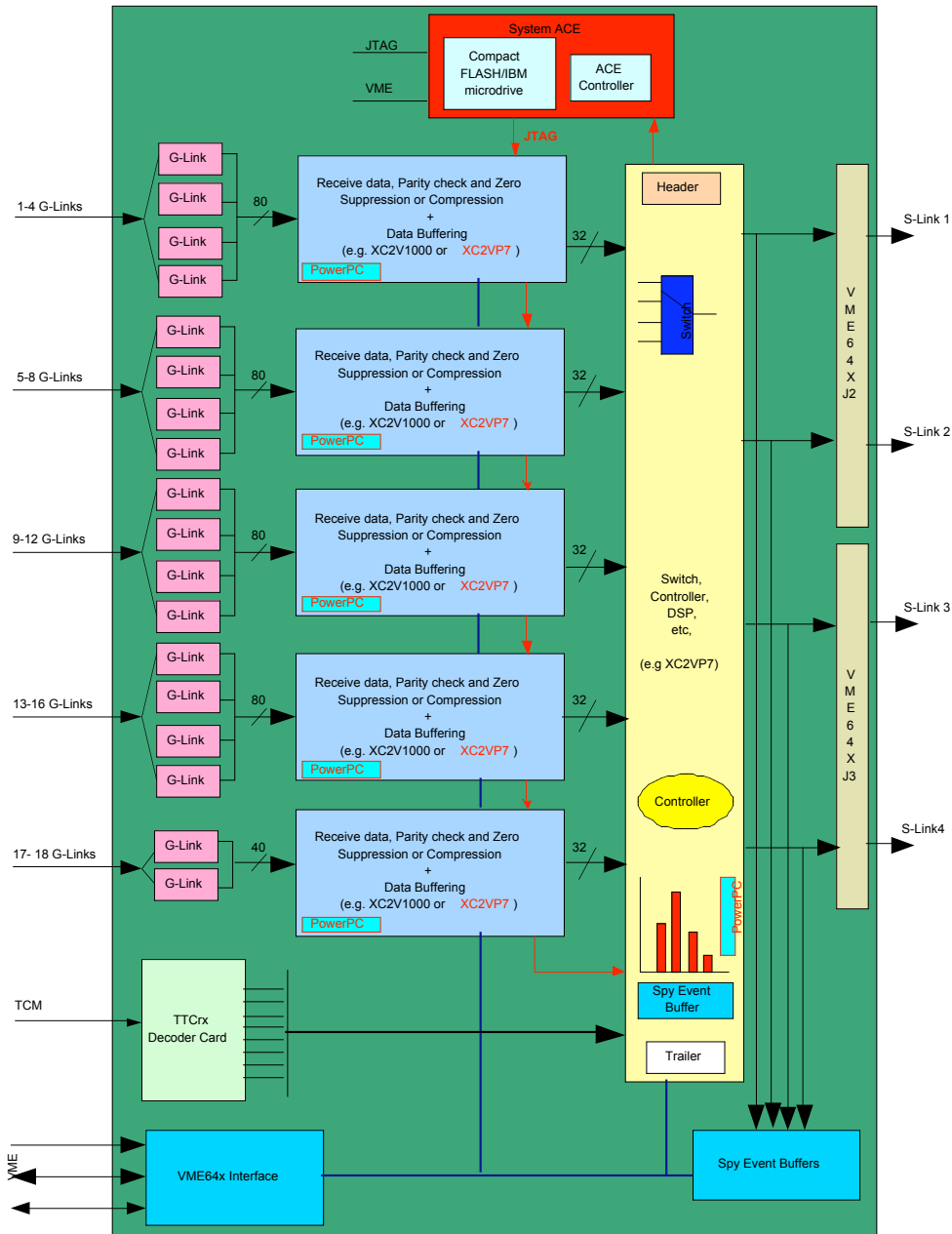
- Reminder - 6U prototype ROD**

G-Link Daughter card
Slink Daughter cards



9U ROD

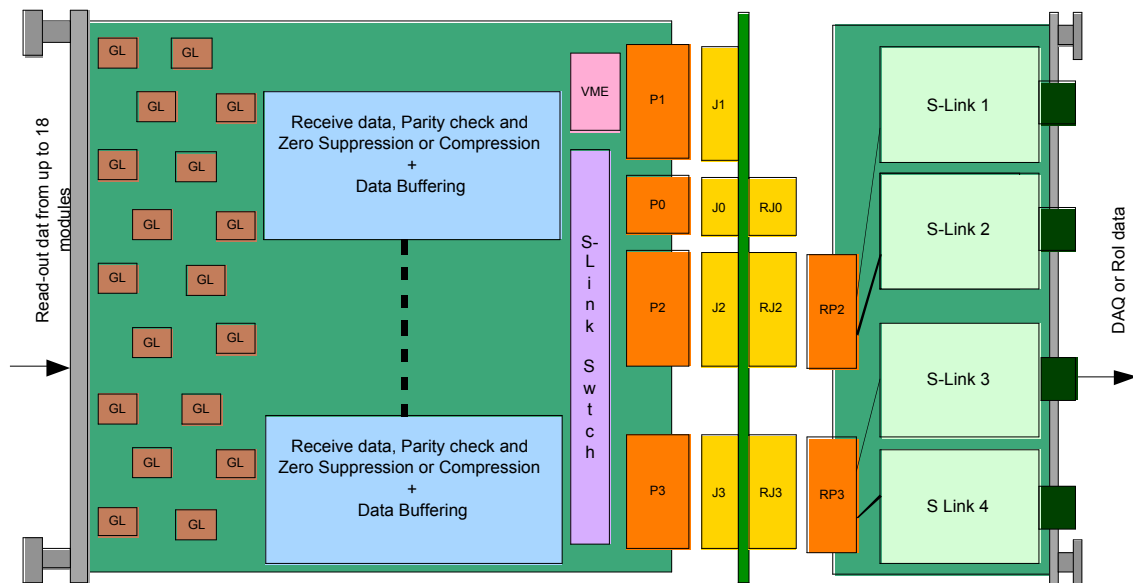
- Block Diagram of 9U ROD



9U ROD

- **ROD to S-Link**

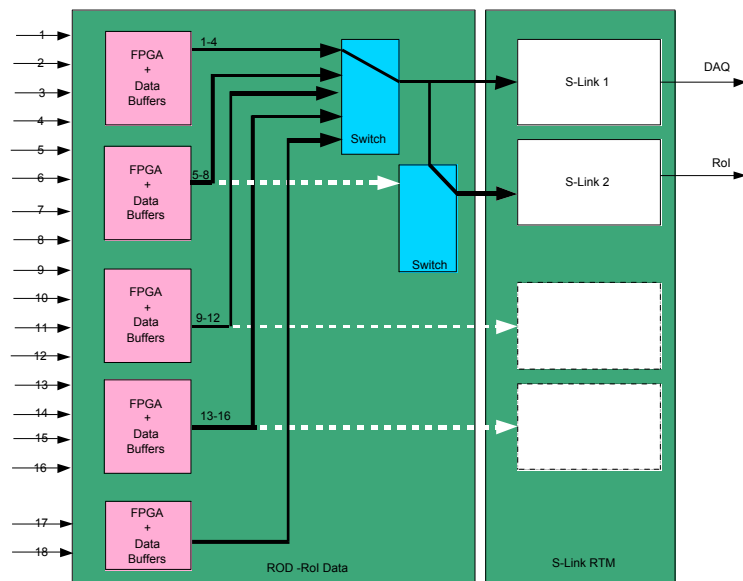
- Four S-Links on VME rear transition module
- Selectable from firmware



9U ROD

- S-Link data switch (any route should be possible via firmware)

RoI data



'Slice' data

