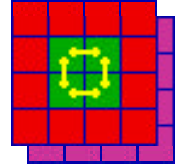




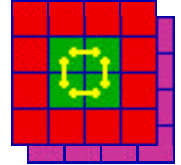
# CPM testing: Glinks, DAQ and RoI



- o Initial tests at RAL
- o Birmingham test setup
- o Problems:
  - o Hardware
  - o Firmware
  - o Software
- o Summary of Results



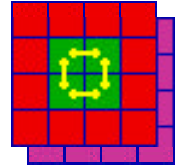
# Initial tests at RAL



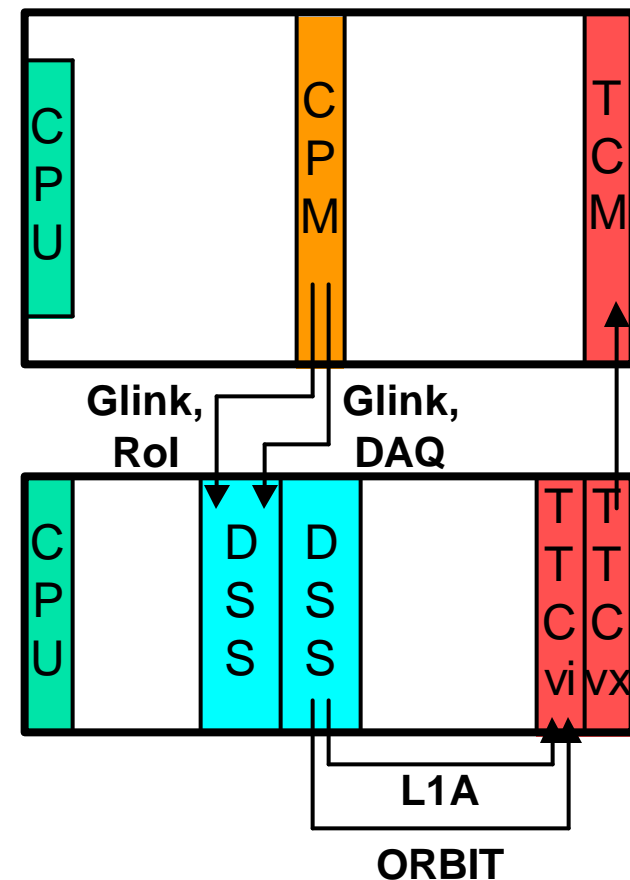
- o Took place mid August
- o First serious use of DSS L1A Generation
  - o Of which more later...
- o Using ROD to DSS slink sink
  - o Plus standard cpRod Test software analysis
- o Correct pattern of RoI s captured
- o Correct DAQ data captured, 1-5 slices
- o Hit data incorrect (firmware formatting)
- o BC Numbers incorrect (no BC reset generated)



# Birmingham Test Setup

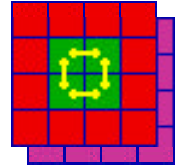


- o DSS (Glink/ECL variant)
  - o Produces L1A and ORBIT
  - o Connected directly into TTCvi
- o TTCvi/TTCvx
  - o Programmed to use external trigger and orbit
  - o Also program B channel 0 to produce BC-reset
- o CPM
  - o Serialiser playback data
  - o (Could also use LVDS DSS)
- o DSS (Glink sink variant)
  - o Accepts any Glink packet
    - o CPM DAQ or RoI
    - o Also tried CMM briefly
  - o Filling controlled by software
  - o Has some advantages over using ROD





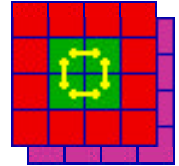
# Problems: Hardware



- CPM #4 DAQ Glink output initially failed
  - Dry joint on chip (not checked by JTAG)
- CPM Glink outputs failed to lock with two CPMs in adjacent slots
  - Noise on power supply
  - Only occurs once serialisers (on other CPM) loaded
  - FIO load from backplane was causing a component to oscillate
  - Richard implemented temporary fix (new design should be OK)
- DSS Glink input instability
  - Could not get new Glink sink cards to lock reliably
  - 'Solved' by reverting to original Glink sink daughter-card
  - Possibly new cards would work with better cables/grounding etc



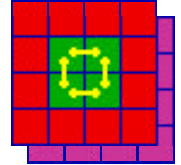
# Problems: Firmware



- Final parity bit on RoI and DAQ inverted
  - Fixed quickly while still at RAL
- Parity bits on 2<sup>nd</sup> and 4<sup>th</sup> slices inverted
- Hit words formatted incorrectly
- Problems with closely spaced triggers:
  - Parity incorrect in RoI stream
  - Data length problem with 5 slices
- Much work needed on readout offsets
  - Three offsets needed:
    - Data into DAQ stream
    - Hits into DAQ stream
    - RoI s into RoI stream
  - Settings need to be stable run-to-run
  - Needed PLD update to decode TTC commands correctly



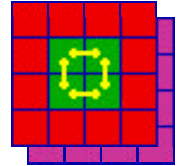
# Problems: Software



- Required better understanding, then new software for TTCvi
  - Signal parity of TTCvi inputs
  - Tuning of TTCvi input timing
  - Generation of BC reset
  - Generation of synchronous playback memory start
- Needed new DSS readout and control mechanism
  - Created 'glink kicker' cf cpm kicker, cpRod kicker
  - No synchronization between readout software and L1As
- Needed new parameters in database
- Some untested software features used for first time



# Summary of Results



- o CPM Glink streams work well on CPM#1 and CPM#4
  - o Tested up to 130 kHz
  - o Tested with closely spaced L1As
  - o Readout offsets stable and (reasonably) predictable
  - o Overnight test had no errors
    - o DAQ stream with 5 slices, about  $10^9$  words tested
  - o Tests with ROD not so extensive, but so far no problems
- o Comments
  - o Tests with DSS Glink sink invaluable
  - o Now have minimal, but sufficient equipment and software tools to fully test a CPM at Birmingham