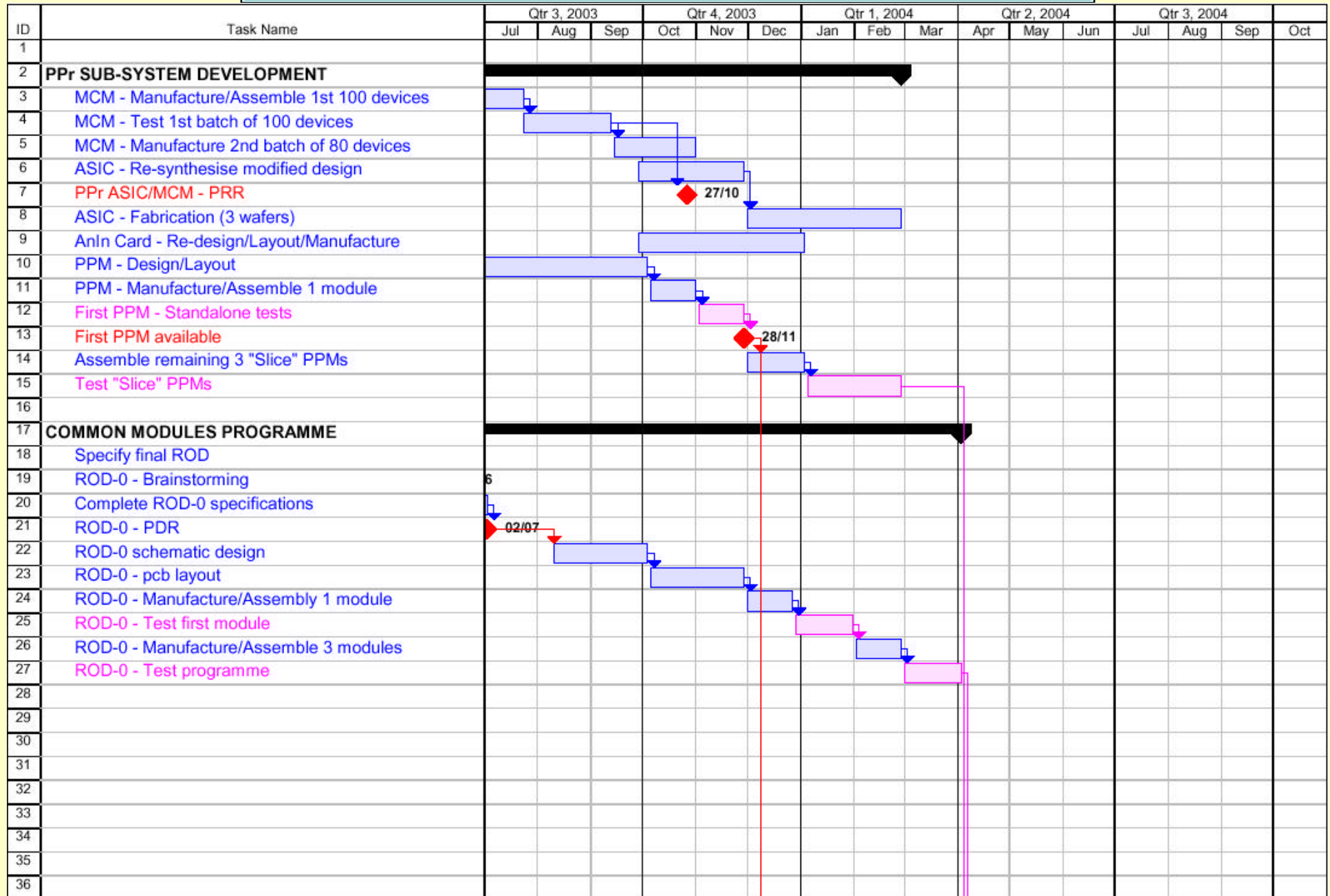


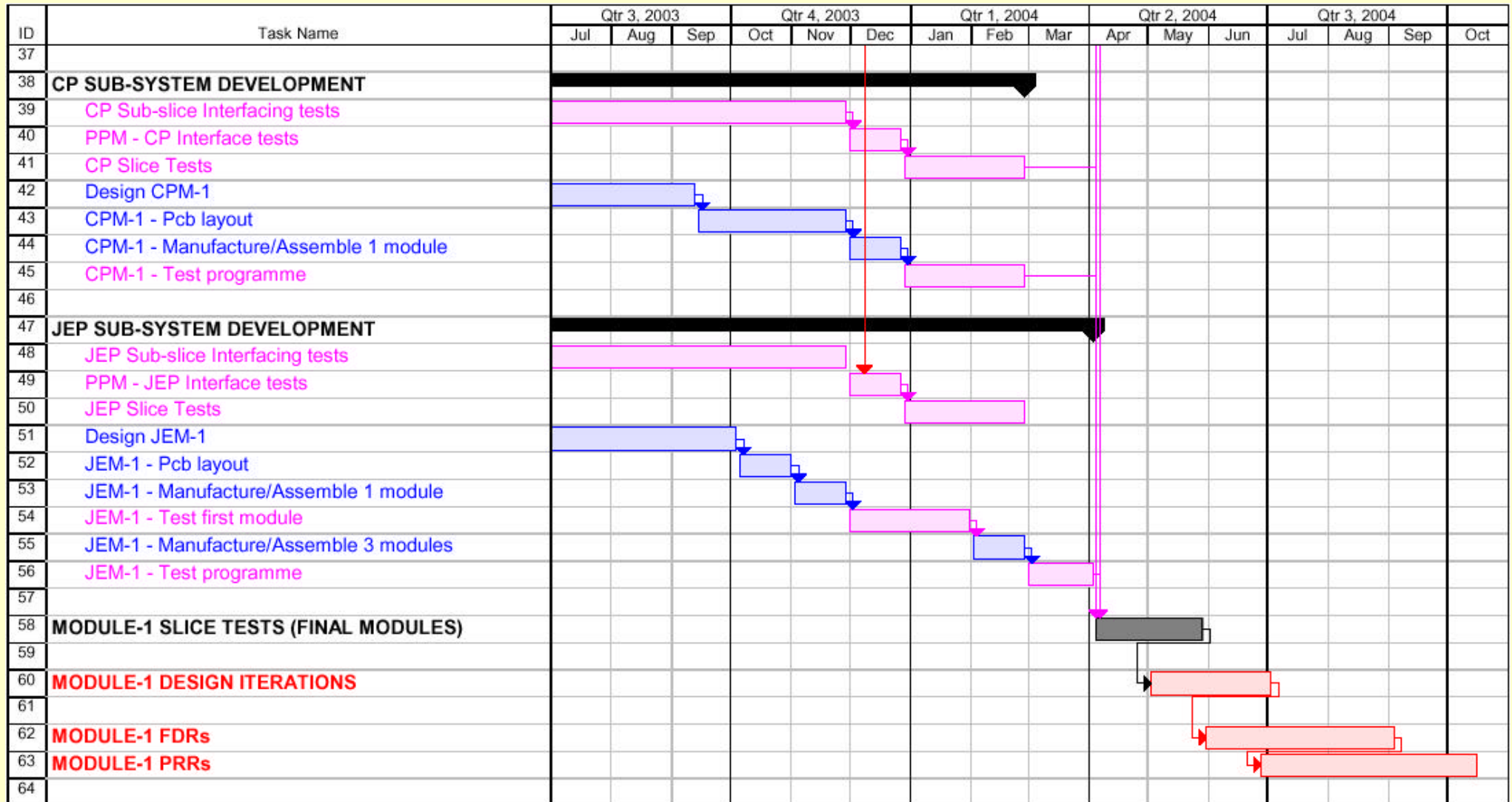
# ***Schedule & Milestones***

***Tony Gillman***

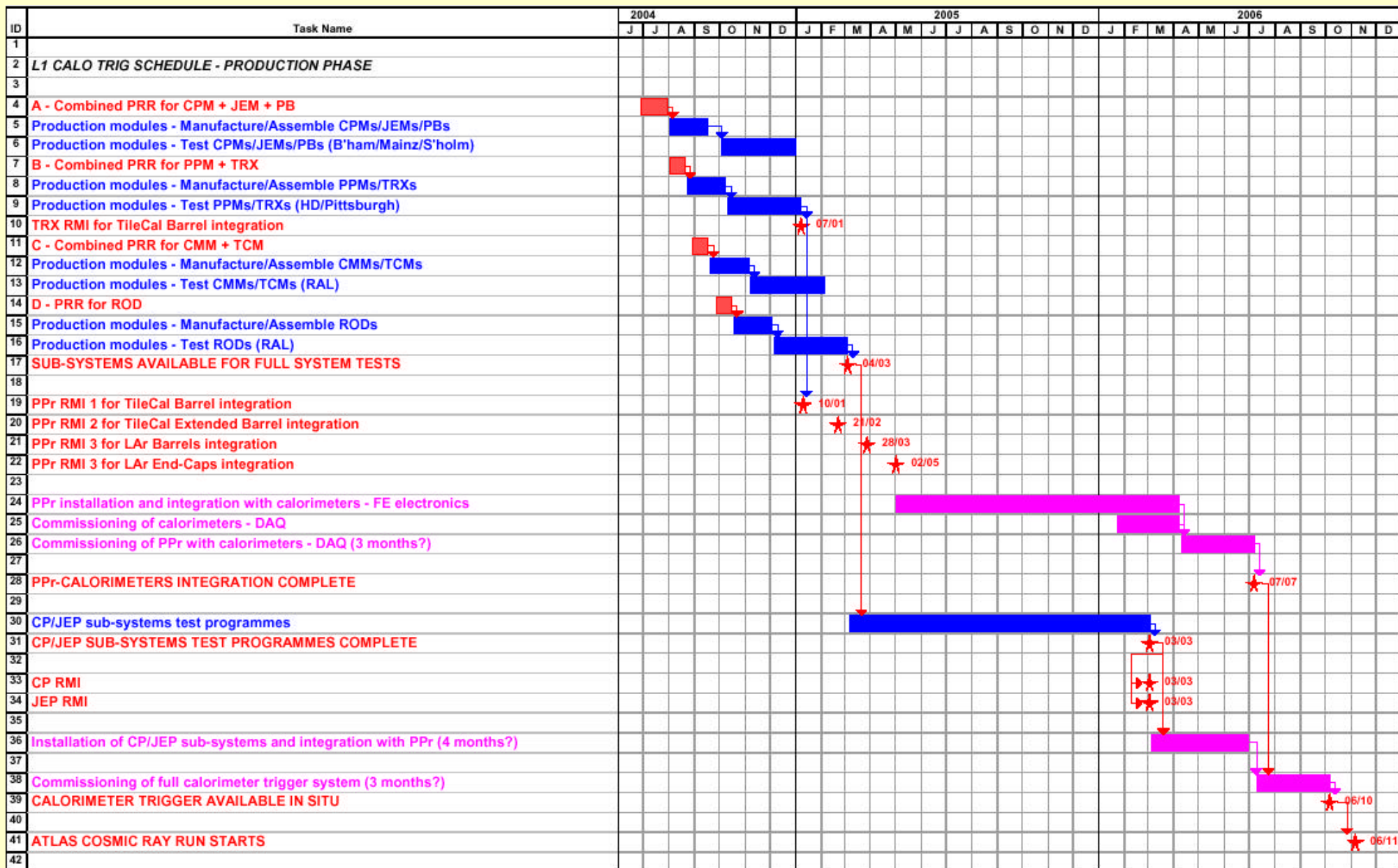
# Pre-PRRs – I



# Pre-PRRs – II



# Production/Installation



# ***Forthcoming FDRs and PRRs***

- ◆ Next important review will be the PPr ASIC and MCM combined PRR
  - ◆ The FDR for the PPr ASIC was held in June 2000, with Erik van der Bij representing TC
  - ◆ The FDR for the PPr MCM was held in April 200, with Philippe Farthouat representing TC
- ◆ According to the “rules”, the combined PRR should now be a relatively straightforward procedure, with minimal documentation required
- ◆ Paul et al are prepared to hold the review in late-October, once Pavel is free
- ◆ **The only complications are:**
  - ◆ The modified ASIC design (*parity error, etc. fixed*) will not be ready for production until sample dies from the new batch of three wafers have been evaluated
  - ◆ The first batch of 100 MCMs gave a yield of only ~60% (*probably largely due to surface defects on Au wire-bond pads*), but production won't start until next batch of 80 substrates has been sampled to check QA
- ◆ **So should the PRR be delayed until after these evaluations?**
- ◆ **We await Philippe's decision...**

# ***Forthcoming FDRs and PRRs***

- ◆ Bill Cleland et al are planning to hold the the FDR for the LAr Receiver module during the LAr Week in November 2003
- ◆ Our recent test-beam measurements suggest that the TileCal Receiver can be an exact copy of the LAr Receiver, so the FDR could be a combined review for both modules
- ◆ The PRR is planned for January 2004, during the next LAr Week
- ◆ The Level-1 Trigger Group has been asked to take part in these reviews – we should decide on some names
- ◆ The CTP has its FDR/PRR in the next few months – we should also participate in these
- ◆ *The main review activity starts ~June 2004, when all remaining modules must be authorised for production*

# Milestones I

<i>Milestone</i>	<i>Date</i>	<i>“Official”</i>
FDR for JEM1	Dec 2002	(Oct 2002)
Start CP/JEP sub-system tests	May 2003	(Oct 2002)
PDR for 9U ROD	July 2003	(Mar 2003)
FDR for Lar/TileCal Receiver	Nov 2003	
PRR for Lar/TileCal Receiver	Jan 2004	
PRR for PPr ASIC/MCM	Oct 2003	(Oct/Nov 2003)
Start full Slice Tests	Dec 2003	(Mar 2003)
“Prototype modules” Slice Tests complete	Mar 2004	(Jul 2003)
“Final modules” Slice Tests complete	June 2004	(Dec 2003)
FDR for CPM	May 2004	(Feb 2004)
FDR for JEM	May 2004	(Mar 2004)
FDR for PB ( <i>Processor Backplane</i> )	May 2004	(Jan 2004)
FDR for PPM	July 2004	(Feb 2004)
FDR for CMM	Aug 2004	(Apr 2004)
FDR for TCM	Aug 2004	(Feb 2004)
FDR for ROD	Sept 2004	(Apr 2004)

# Milestones II

<i>Milestone</i>	<i>Date</i>	<i>“Official”</i>
PRR for CPM	July 2004	(Feb 2004)
PRR for JEM	July 2004	(Apr 2004)
PRR for PB	July 2004	(Feb 2004)
PRR for PPM	Aug 2004	(Feb 2004)
PRR for CMM	Sept 2004	(May 2004)
PRR for TCM	Sept 2004	(Feb 2004)
PRR for ROD	Oct 2004	(May 2004)
Sub-systems production complete	Mar 2005	(Oct 2004)
PPr RMI-1 for TileCal Barrel integration	Jan 2005	
PPr RMI-2 for TileCal Extended Barrel integration	Feb 2005	
PPr RMI-3 for LAr Barrels integration	Mar 2005	
PPr RMI-4 for LAr End-Caps integration	May 2005	
Finish PPr test programme	May 2005	(Apr 2005)
Finish CP/JEP test programmes	Mar 2006	(Oct 2005)
Finish PPr-Calorimeters integration	July 2006	(Oct 2005)
<i>Calorimeter trigger available in situ</i>	<i>Oct 2006</i>	<i>(Apr 2006)</i>



# ***The clock is still ticking ...***

- |   |               |                           |
|---|---------------|---------------------------|
| ◆ ATLAS cosmic ray run starts               | November 2006 | <i><b>(38 months)</b></i> |
| ◆ Full calorimeter trigger system available | October 2006  | <i><b>(37 months)</b></i> |
| ◆ Installation of fully-tested CP & JEP     | March 2006    | <i><b>(30 months)</b></i> |
| ◆ Installation & integration of PPr starts  | May 2005      | <i><b>(20 months)</b></i> |
| ◆ Manufacture of all sub-systems complete   | March 2005    | <i><b>(18 months)</b></i> |
| ◆ Module production starts                  | August 2004   | <i><b>(11 months)</b></i> |
| ◆ Start FDRs and PRRs for all final modules | June 2004     | <i><b>(9 months)</b></i>  |
| ◆ Start final module design iterations      | May 2004      | <i><b>(8 months)</b></i>  |