

CMM hardware and firmware status

Status of...

- Current CMM
- Next generation of CMM
- Firmware: CP, Jet, JEM
- Specification



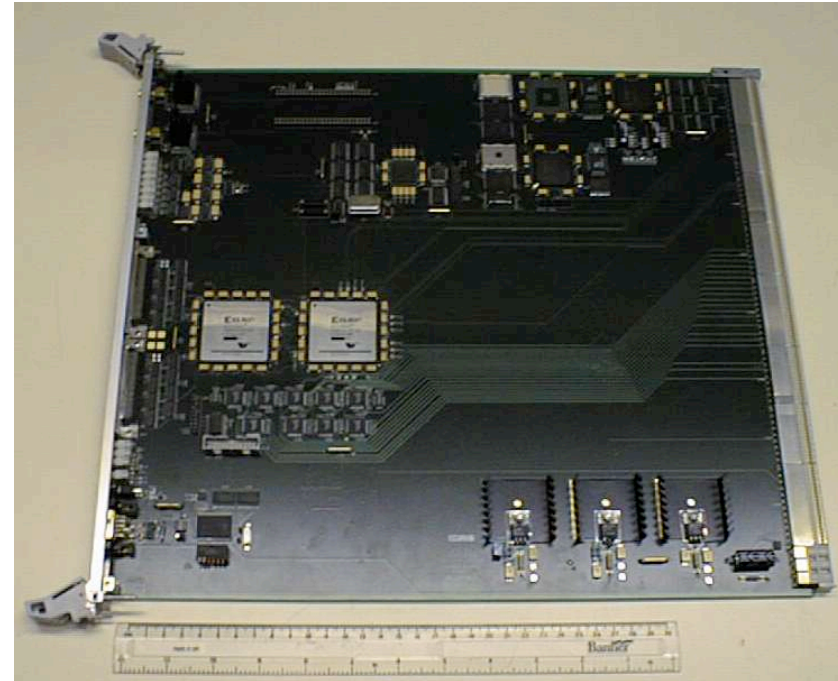
Current CMM

Tested:

- Real-time data path
- Readout \square ROD
- FPGA configuration from FLASH
- TTCrx, rad- & non rad-hard
- I2C interface to TTCrx

- Investigating problem of unstable Geographic Address in Control Mode Register (firmware problem)

- No other outstanding tests



Next Generation of CMM

- Design in D/O for board layout
- Few components added, changed
 - Compared to current CMM, Largest change is addition of strengthening bars
- Placement finished
- First pass at auto-routing went well
- D/O should be finished end of next week



CMM Firmware

- CPM hit counting:
 - Firmware complete
 - No known problems
- Jet hit counting:
 - To be done by Sam Silverstein
 - based on CPM hit code
 - Ian to send CPM hit Code to Sam
- Jet Energy Summing:
 - Real-time path designed by Andrea Dahlhoff
 - Andrea to send code to Ian, who will add Readout logic modified from CPM hit firmware
 - Ian to send code to Andrea



CMM Specification

- Additions to CMM address space:
 - FPGA Configuration & Control registers
 - TTCrx I2C interface & dump capture

... Updates to specification about to arrive in Norman's in tray.

