

moduleServices Status

DSS

- Polymorphism:
 - Redefined to simplify daughter card code ...
 - Allows simple implementation of 2 link cards of arbitrary type.
 - But ... the price is “switches” in the code.
 - Code configures according to fpga firmware type:
 - Lvds, G-link
- Channel indices now run 0...7

Ttcvi

- Module timing parameter setup:
 - Interim ... until all TTCDECs have I2C.
 - Information (rx address, skew, delay)
 - Retrieved from database (M.L.)
 - Address from configuration db, data from calibration db.
 - Invoked by long command register in vi
 - But, implementation naïve:
 - Changes should be gradual.
 - This requires knowledge of state

Module Services

- DaqInterface methods:
 - Defined new methods
 - identify()
 - selfTest()
 - pause() ; resume()
 - unload() ; deconfigure()
 - setModuleStatusPointer() ;
getMyModuleStatusPointer() ;
updateModuleStatus() ;
 - Provided skeleton implementations.

Tidy up

- Removed legacy code from testVector package
 - bill-stokes simulation, bmb simulation for CP-Roi. Support code.
- Code compilation clean
 - No more warnings
- Added doxygen macro to cmt requirements.
 - (a la M.L. and S.J.H.)