



CCLRC
Rutherford Appleton Laboratory

CMM status

- **Developments since last meeting.**

CMM-Energy Version

- Bug found in VME access to Crate Output RAM:
 - Unrelated block of FIFO was mapped to the same address.
 - Fixed firmware ready to be loaded on to CMMs.
 - VME access to Andrea's algorithm block has been reworked
 - Unified with rest of design.
 - E_T^{miss} LUTs now mapped directly to VME
(previously write-only access via single location + address counter)
 - Overhaul 90% complete; need agreement on details to proceed further.
 - Problem with VME access to ET threshold registers can't be reproduced outside Lab 12. Software? Faulty board?
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CMM, all versions

- Readout Firmware
 - Current ROD design requires deadtime between readout events (of minimum length dependant on ROD firmware version).
 - Easy to modify CMM firmware to insert such deadtime.
 - No action taken yet. Change Request should be made if required.
- Fifth CMM, (number 1) awaits commissioning.

