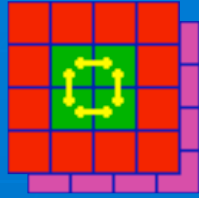


Slice Testing

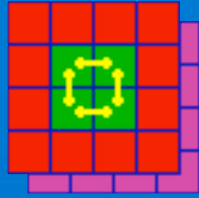
Priorities & Plans



Overview



- ! CP
- ! JEP
- ! PPM
- ! Backplane
- ! 9U ROD
- ! Other Modules
- ! Test Beam Synchronisation

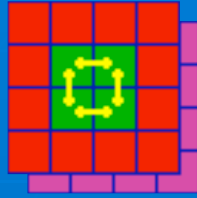


CP



! CP

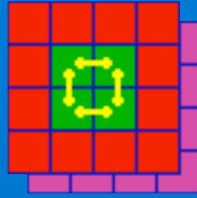
- A few remaining basic issues:
 - ! Implement a maximum number of CPM configuration.
 - ! Prepare checklist and revisit it:
 - Timing setup.
 - BCNUM matching.
 - Operation at minimal L1A separation.
 - Multi-slice operation.
- Add a couple of CPMs and we can start some real tests.
- And one of these days, we'll even have a PPM!



JEP



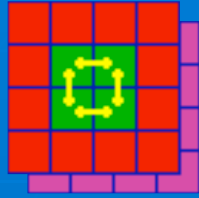
- ! Await new module:
 - Use Jem 0.1 to debug
 - ! CMM firmware.
 - JET variant, in particular.
 - ! RoI ReadOut and event matching.
- ! JEM 1.0
 - Available Mid May, early June:
 - ! Time to schedule tests soon.
 - Then:
 - ! Reintegrate with ROD, CMM.
 - ! When will a 2nd JEM be available?
 - Requirements:
 - ! Software.
 - ! 6u ROD firmware for RoI.
 - ! G-Link Optical-Electrical converters
- ! Actual Status in terms of slice-test progress?
 - **Snookered.**



PPM



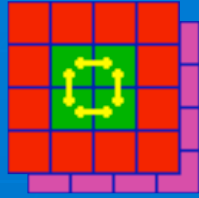
- ! Early Front End integration:
 - Needs to be scheduled in conjunction with JEM, CPM
 - To take place in Mainz ASAP.
 - Date to be agreed.
- ! Test beam in June:
 - Takes the "front seat"
 - This by definition leaves slice tests in the "back seat".
- ! Multiple new PPMs
 - Available soon.
 - ! When will firmware be mature.
 - ! When will all the software components be available to allow integration?
 - ! Difficult to schedule when there are not many dates to work from!
 - ! Middle June?



Backplane



- ! Backplane working group
 - established after Heidelberg.
 - Request for input from Sam.
 - Not much more activity, yet.
- ! Testing.
 - Systematic work to proceed in BRUM.
 - Orthogonal series at RAL?
- ! Repair of bent pins.
 - Not yet demonstrated!

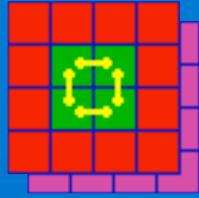


9U Rod



! 9U ROD

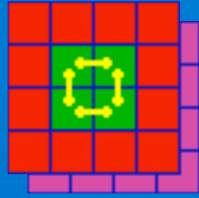
- Just around the corner!
 - ! Something ready to plug-in in July.
 - ! Software work needs to start soon.



Other Modules



- ! TCM & VMM:
 - Are there tests which are missing or unfinished?
 - ! CAN.
 - ! Reset functionality.
 - ! Clock integrity.
 - ! Dual-port use.



Test Beam Synchronisation



- ! Commitments start in July:
 - What Testing work can go on in parallel:
 - ! Largely JEM and 9U ROD. These will likely be late arrivals to the test beam.
 - ! Systematic CP tests from the “checklist” -
 - As a part of the programme
 - When beam not available.
 - How to minimise disruption?
 - ! Do initial DAQ integration with DSS and ROD, as suggested at Heidelberg?
 - ! Delay shipping date. Then change “mode”.