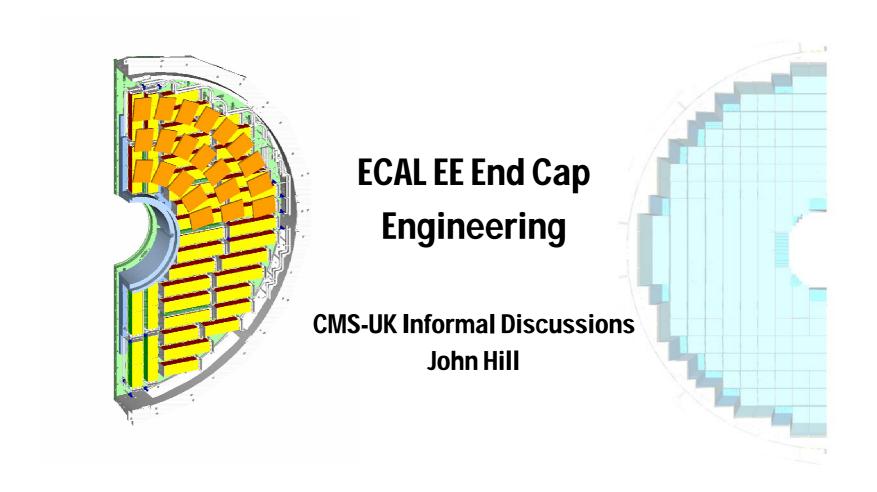


#### **EE End Cap Engineering**

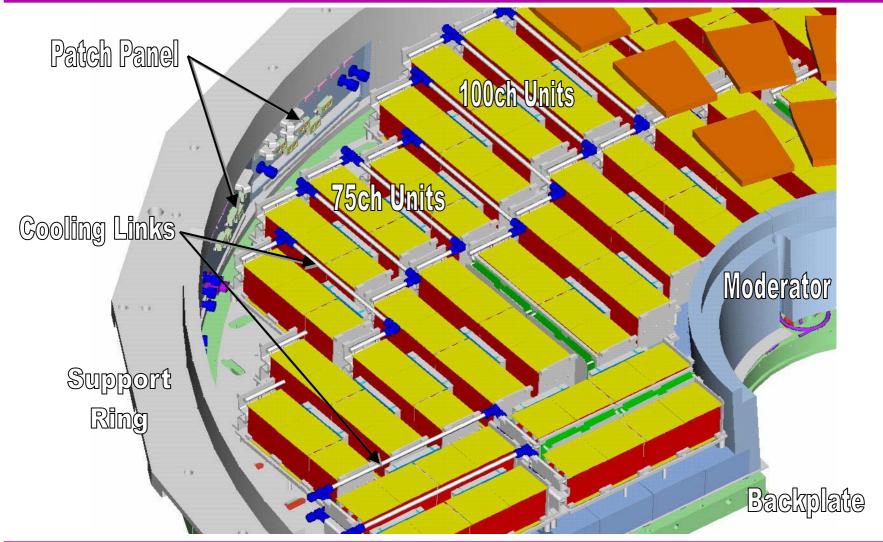






#### **EE End Cap Internal Layout**

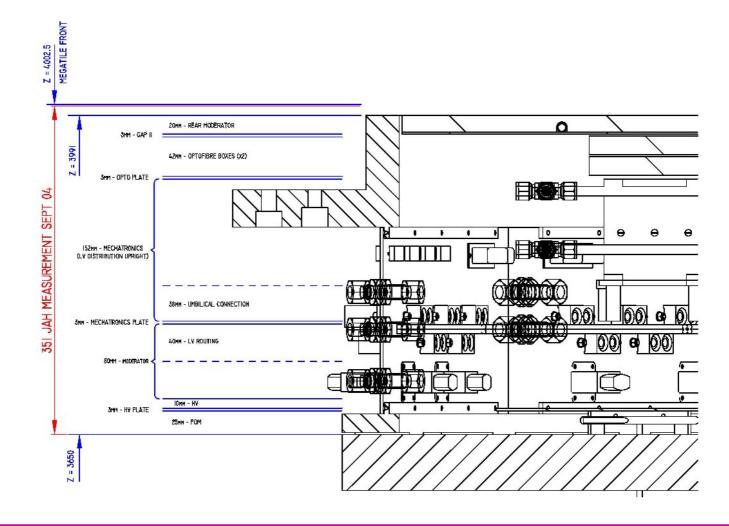






#### **ECAL EE Endcap Z Positions**

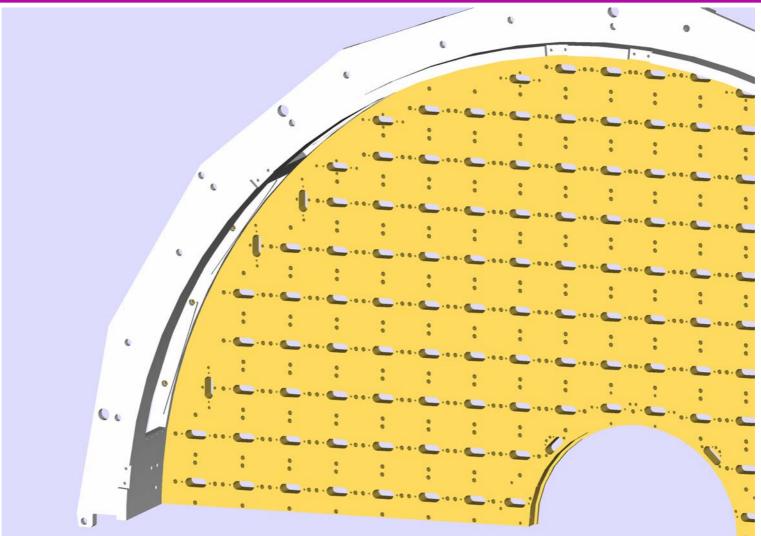






## Build – Backplate and Support Ring

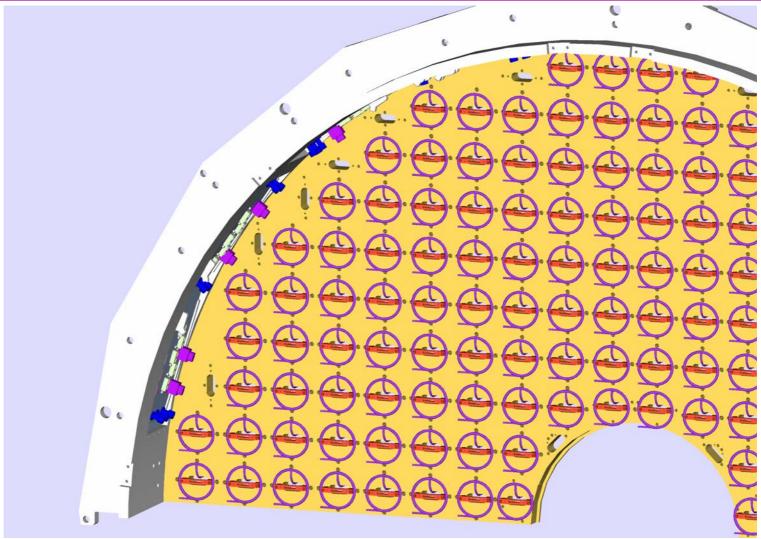






### Build - Fibre Optic Monitoring

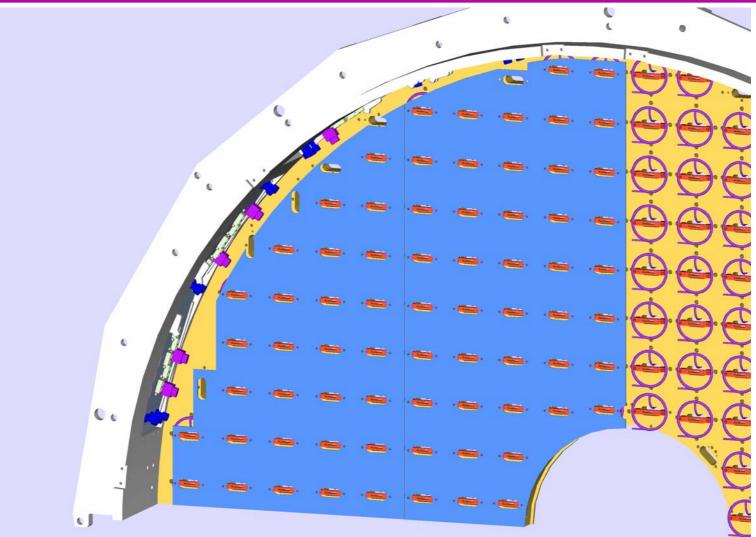






### Build - HV support/routing

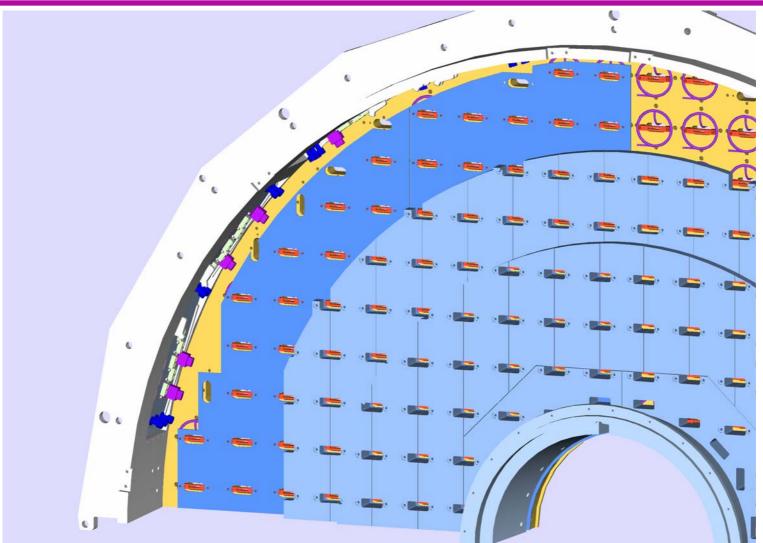






#### Build - Front/Inner Moderator

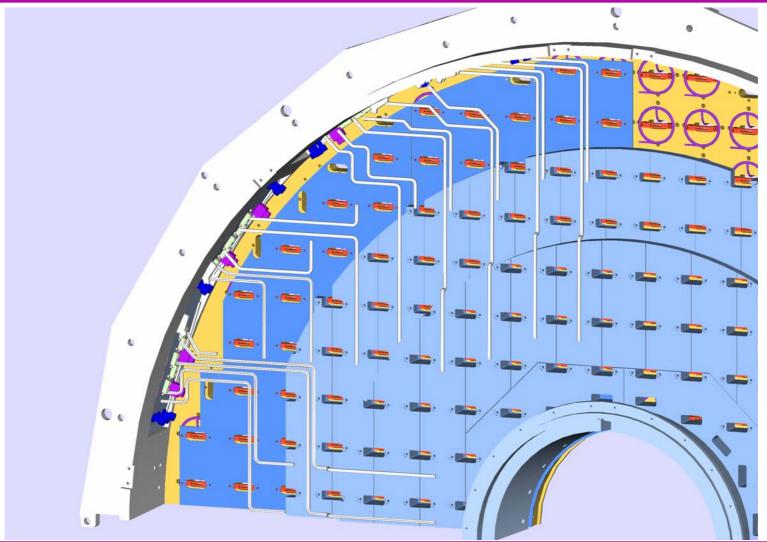






## Build – LV and support (support plate not shown)

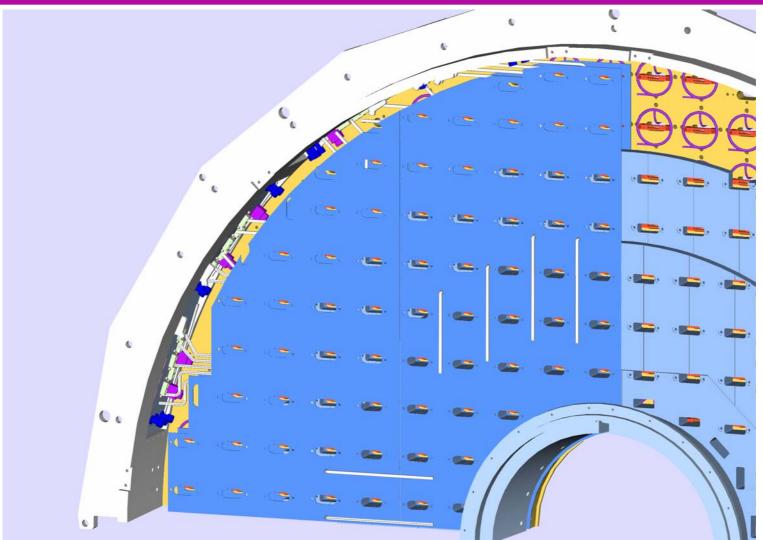






#### **Build - Electronics Support**

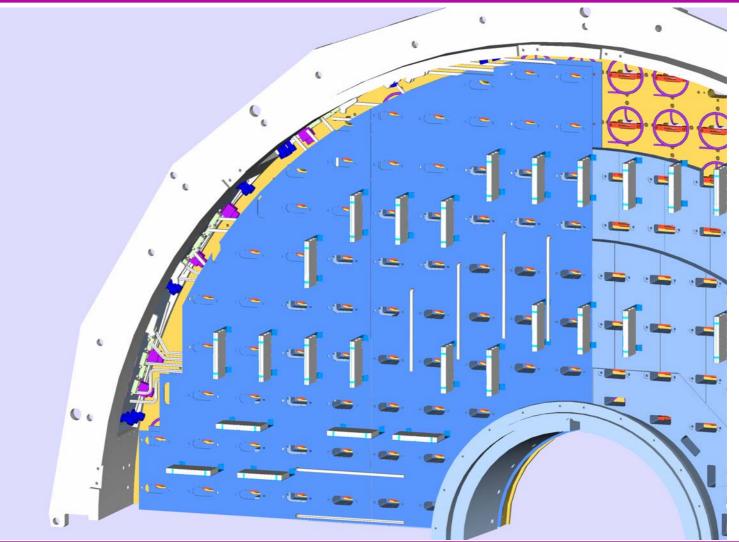






#### Build - LV distribution







## Build – Umbilical attachment, mothercard support frames

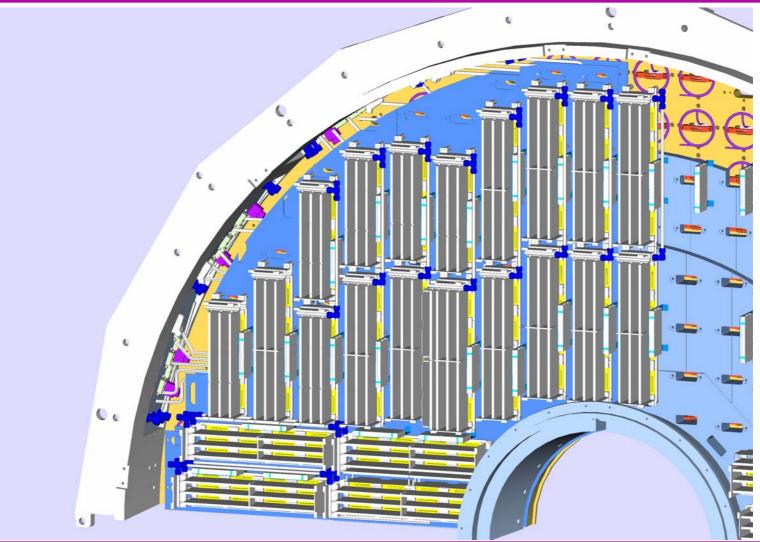






#### **Build - Cooling bars**

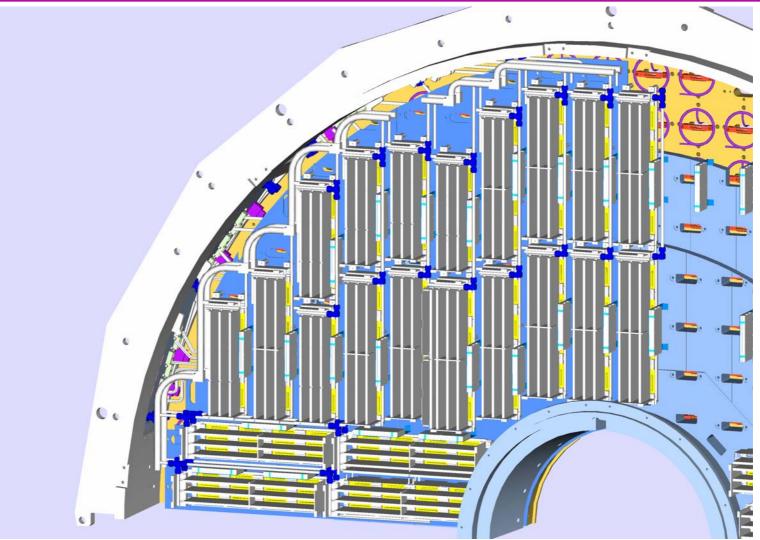






#### Build - Cooling manifolds

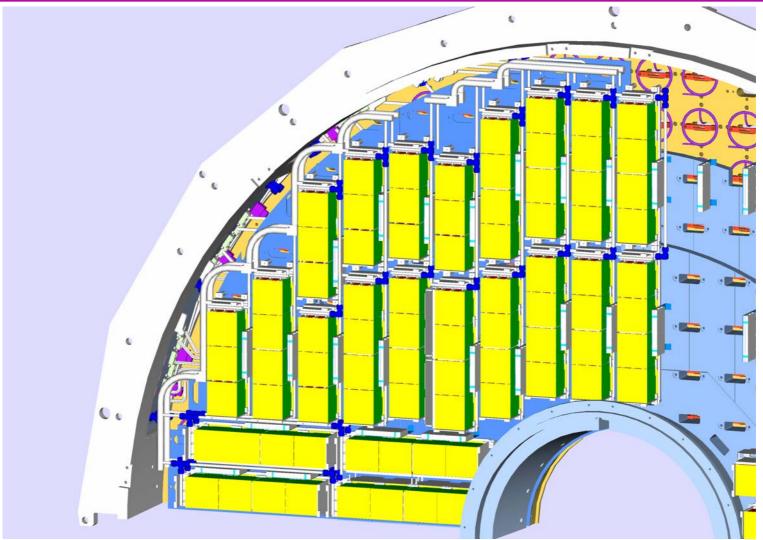






#### Build - VFE Pcb's/TRLB

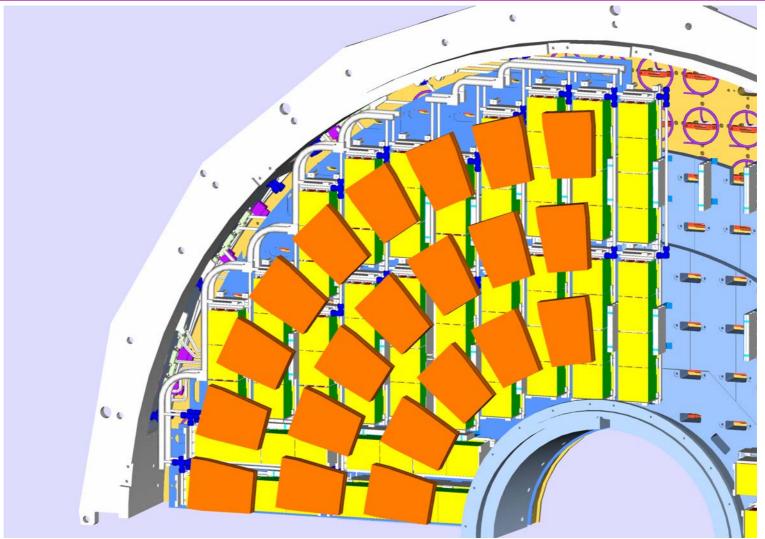






#### Build - Optolink box units







# Build - Rear moderator, complete!



