



19th April 2001

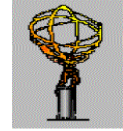
CMM Spec Update



C .N .P .Gee
Rutherford Appleton Laboratory



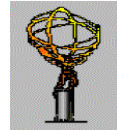
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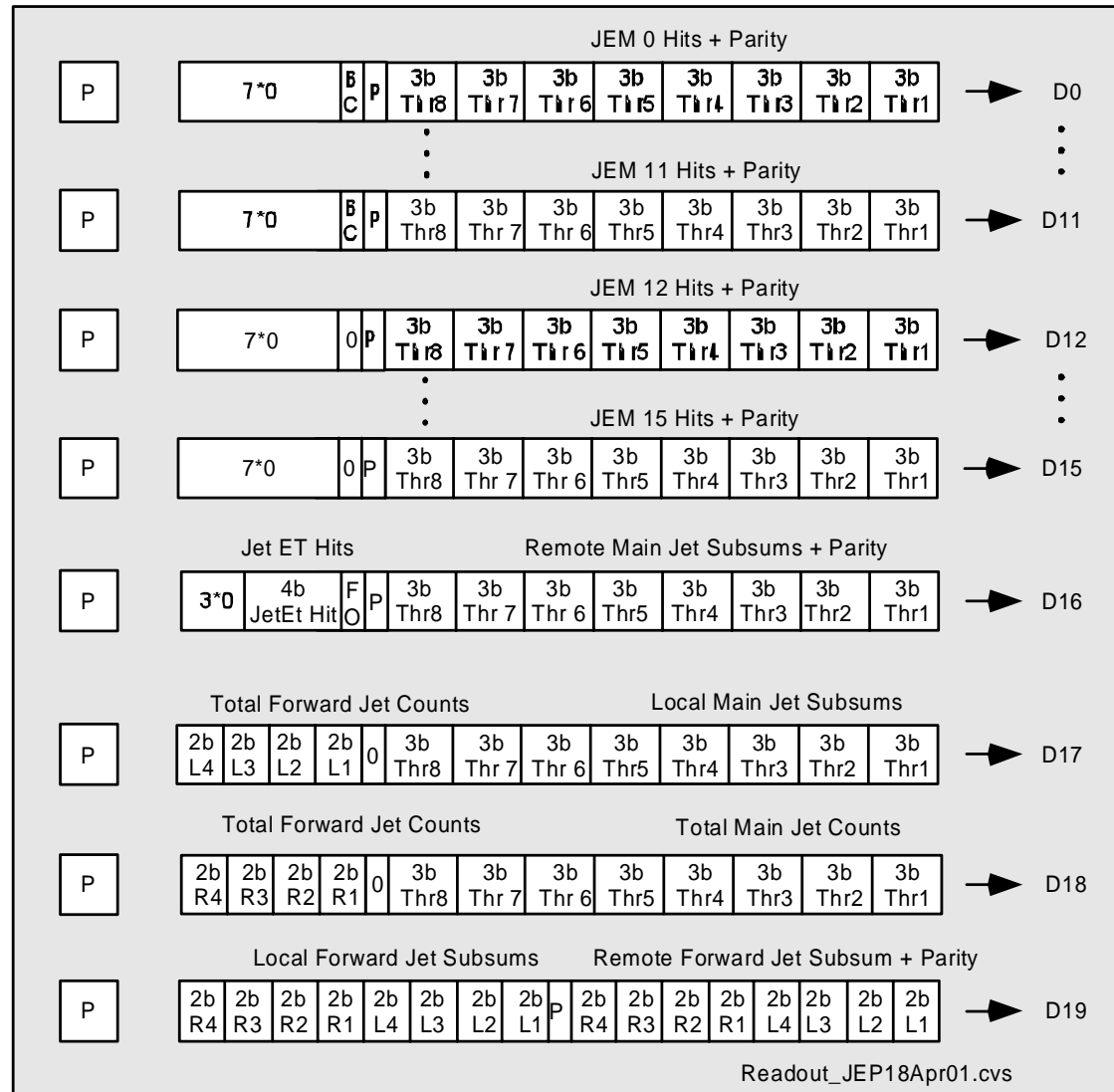


Jet ET hits readout and RoI



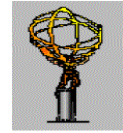
4 Jet ET hits here >

For RoI, only the last 8 +P ticks of the frame are needed.





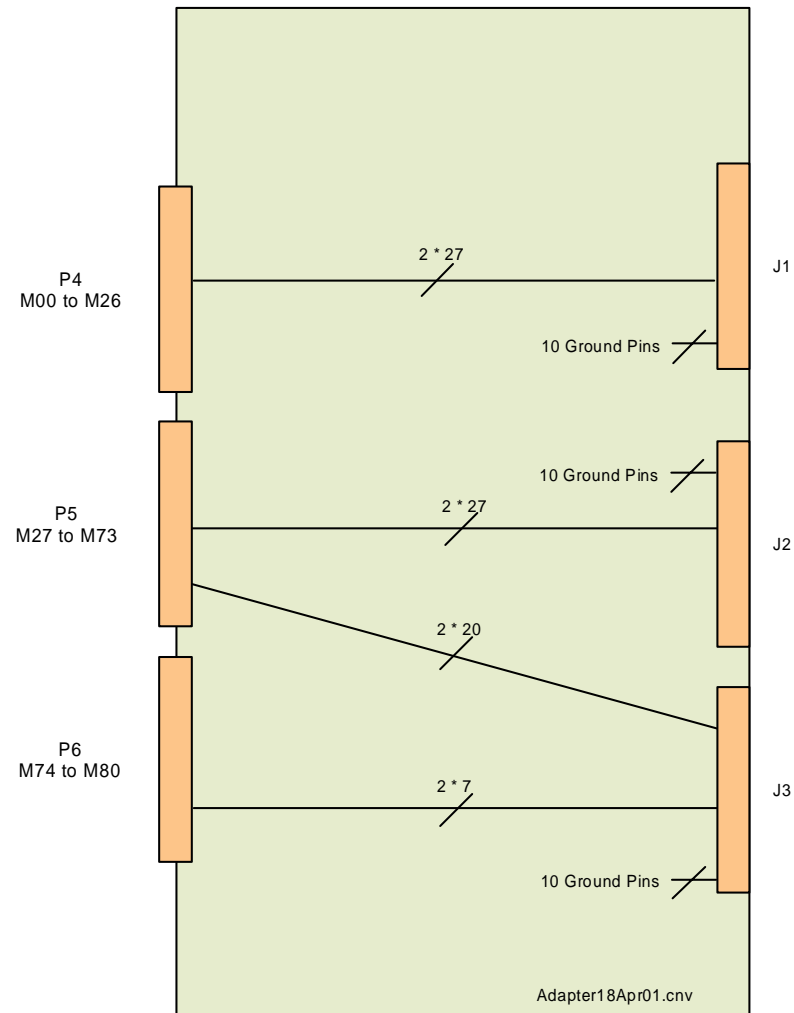
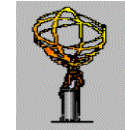
Backplane Pinout



- **As defined in Backplane spec 0.9b2.**
- **Provides all required inputs from CPMs/JEMs**
- **Also provides connections to other CMMs:**
 - System requires $3 \times 25 = 75$ pairs.
 - 0.9b2 provides $3 \times 27 = 81$ pairs on connectors 4,5,6.
 - Signal M74+ on connector 5, M74- on connector 6.
 - May not matter, since we will use adapter board for cables.
 - Sam informed anyway, invited to move M74+ to connector 6.

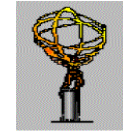


Backplane/Cable Adapter Board





TTC Registers matching CPM



TTCrx Control Register

Bit	Descriptive Name	Signal name
0-7	<i>Data to TTCrx</i>	
8	<i>Write</i>	0 = read, 1 = write
9-13	<i>TTC Register Number</i>	
14	<i>Unused</i>	
15	<i>Reset TTCrx Controller</i>	

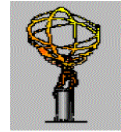
TTCrx Status Register

Bit	Descriptive Name	Signal name
0-7	<i>Data from TTCrx</i>	
8	<i>I2C Busy</i>	
9	<i>I2C Error</i>	
10-15	<i>Unused</i>	

Can we copy the I2C controller code from CPM to CMM?



Spec Completion



- **Sam to confirm Backplane detail.**
- **Then can be finished.**