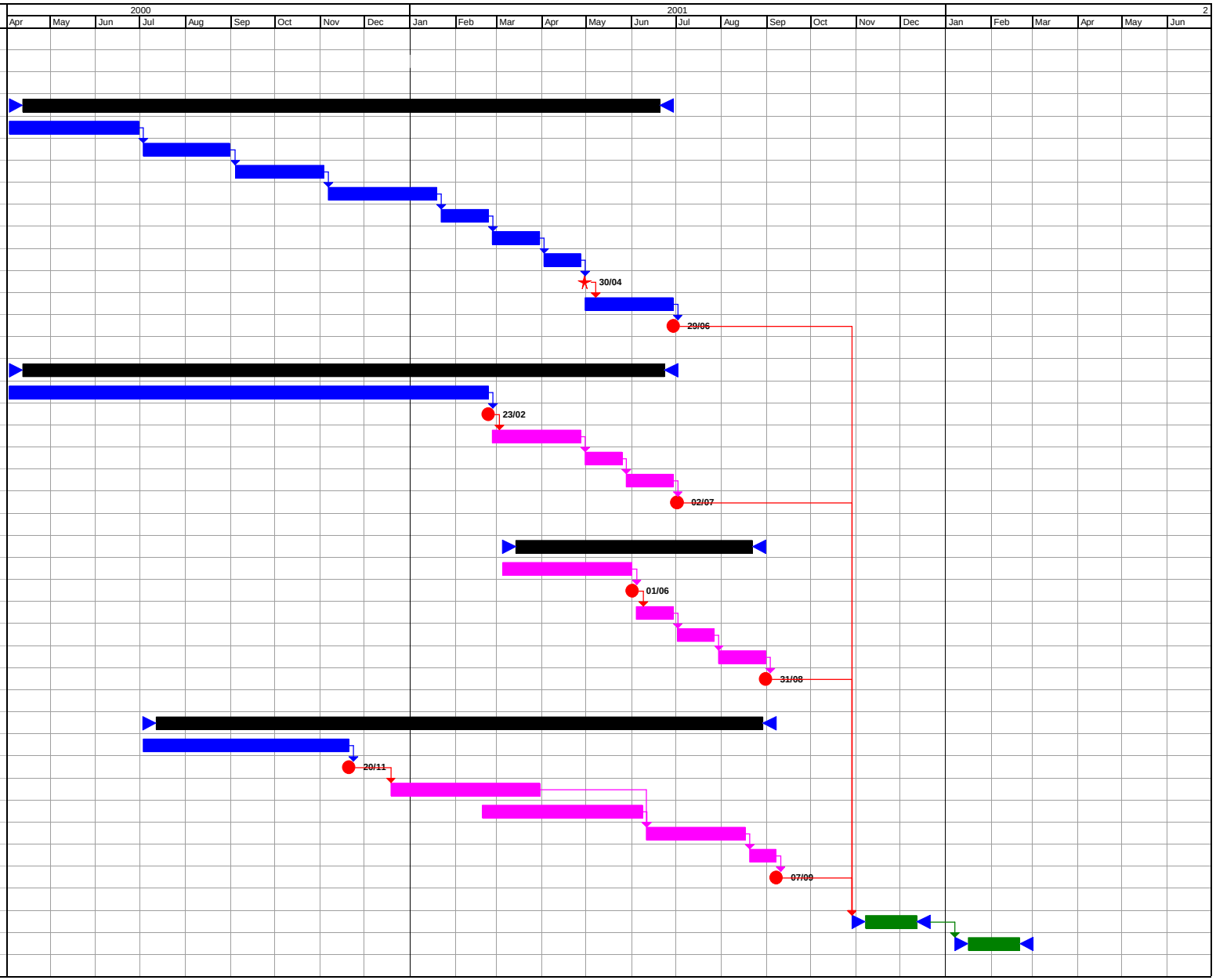
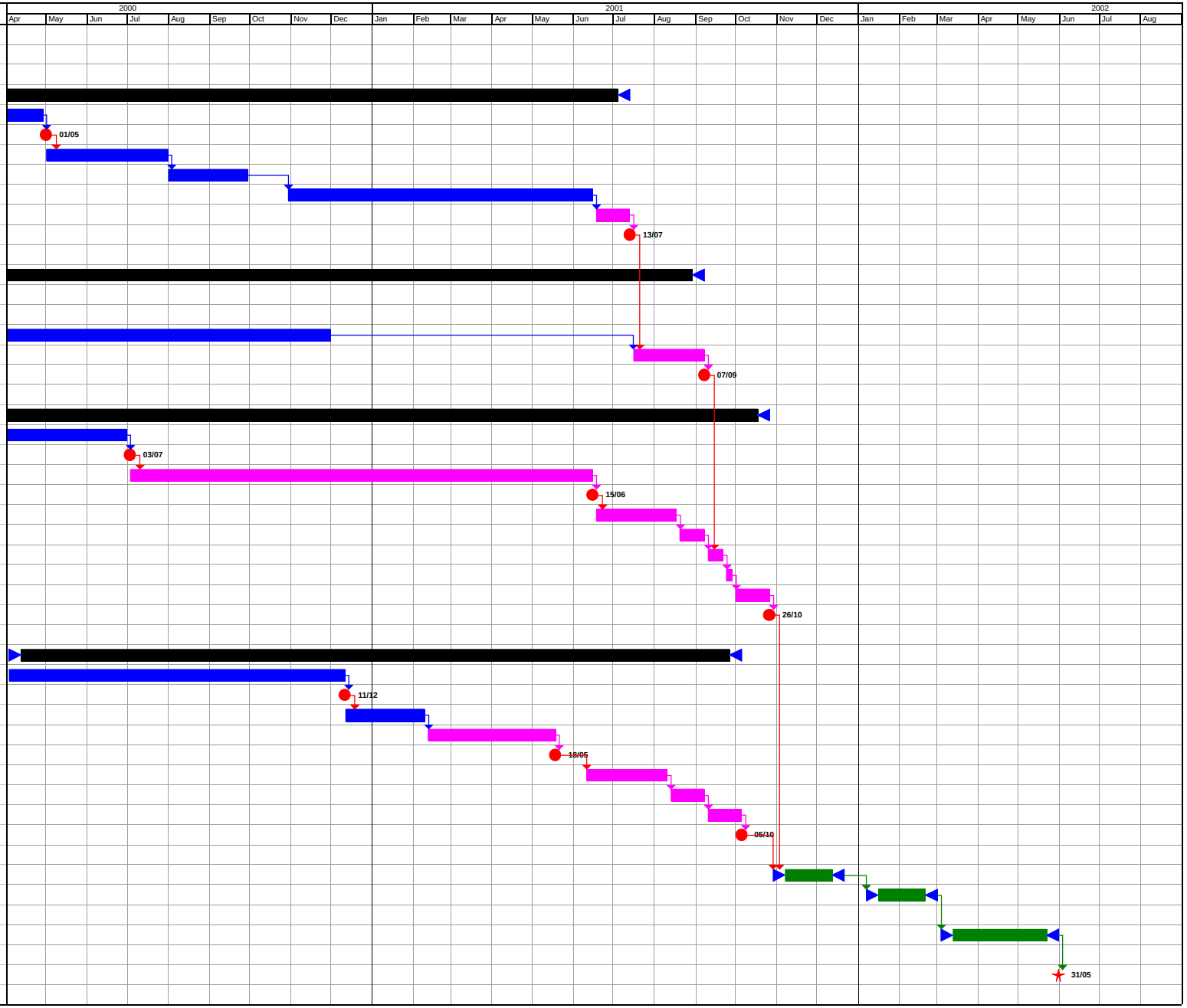
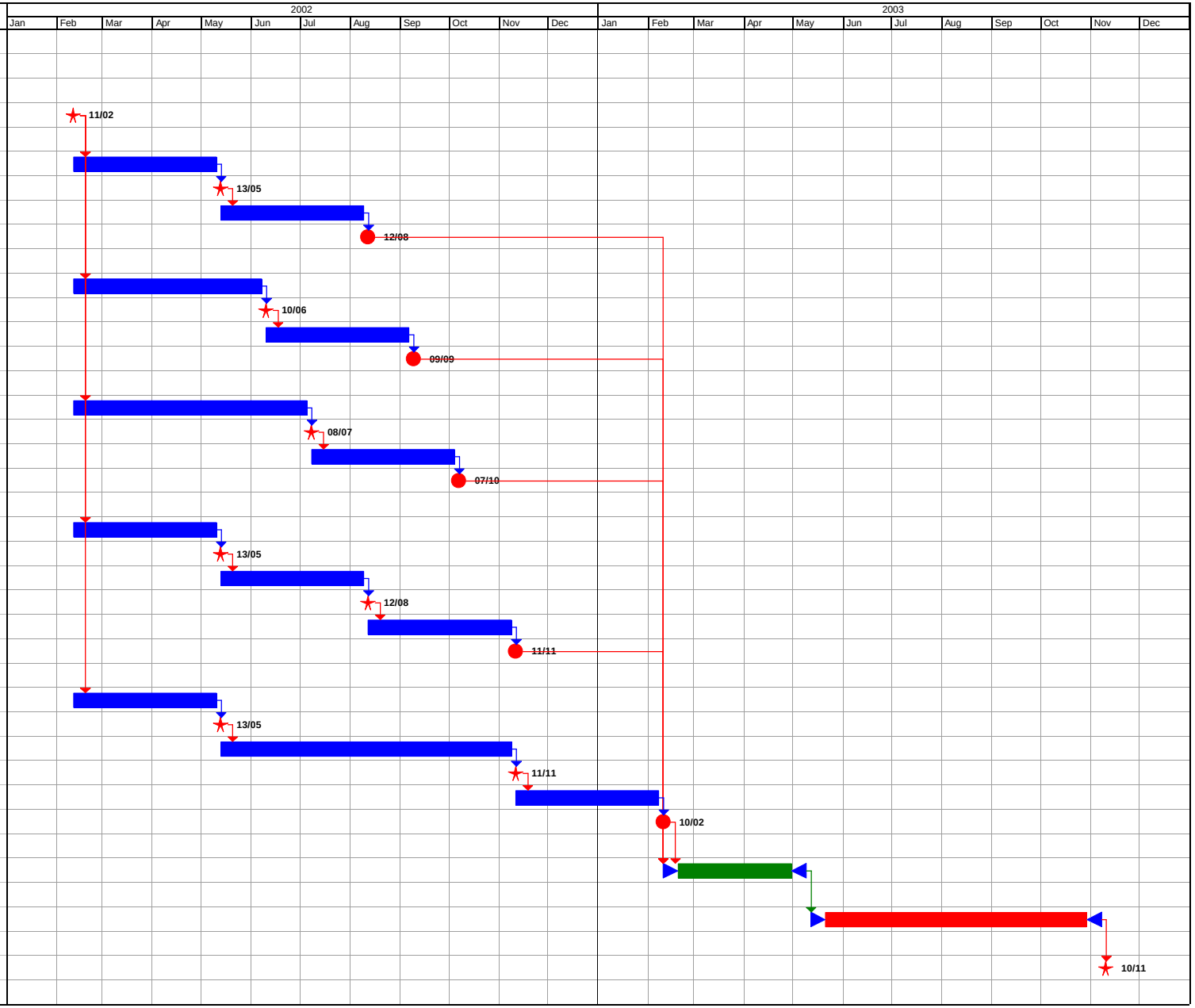
ID	Task Name
1	
2	***CP SYSTEM DESIGN PROGRAMME - 1***
3	
4	**P-ROD Prototype**
5	Layout
6	Manufacture
7	Engineering tests
8	DAQ system tests 1 (DSS <-> P-ROD)
9	Prepare RoI FPGA code
10	RoI system tests at RAL (DSS <-> P-ROD)
11	RoI Builder tests at CERN (DSS -> P-ROD -> RoI-B)
12	***RoI-B integration tests complete (Milestone)***
13	DAQ system tests 2 (DSS <-> P-ROD)
14	***P-RODs available for all sub-system tests***
15	
16	**TCM Prototype and Adapter Link Card (ALC)**
17	Design
18	Schematic review
19	Layout
20	Manufacture
21	Standalone tests
22	***TCMs available for all sub-system tests***
23	
24	**VME CPU Personality Card (CPC)**
25	Design
26	Schematic review
27	Layout
28	Manufacture
29	Standalone tests
30	***CPCs available for all sub-system tests***
31	
32	**P-Backplane (PB)**
33	Specifications
34	PDR
35	Layout
36	Tendering for manufacture
37	Manufacture
38	Standalone tests
39	***PBs available for all sub-system tests***
40	
41	**CP sub-system tests (RAL)**
42	**CP sub-system tests (RAL)**
43	



ID	Task Name
1	
2	***CP SYSTEM DESIGN PROGRAMME - 2***
3	
4	**Generic Test Module (GTM)**
5	Specify
6	***PDR***
7	Design
8	Layout
9	Manufacture
10	Standalone tests
11	***GTM available for CP/Serialiser FPGA tests***
12	
13	**CP-FPGA**
14	Specify
15	***PDR***
16	Design firmware
17	Test with Serialiser FPGAs (using GTM)
18	***CP/Serialiser FPGAs available for CPMs***
19	
20	**CPM Prototype**
21	Specify
22	***PDR***
23	Schematic design
24	***Schematic review***
25	Layout
26	PCB manufacture
27	Assembly
28	JTAG tests (RAL)
29	Standalone tests (Birmingham)
30	***CPMs available for CP sub-system tests***
31	
32	**Common Merger Module (CMM)**
33	Specify
34	***PDR***
35	Update specifications
36	Schematic design
37	***Schematic review***
38	Layout
39	Manufacture
40	Standalone tests
41	***CMMs available for sub-system tests***
42	
43	**CP sub-system tests (RAL)**
44	**CP sub-system tests (RAL)**
45	
46	**Phase 1 "Slice" tests (Heidelberg) - 3 months**
47	
48	***Phase 1 "Slice" tests complete (Milestone)***
49	



ID	Task Name
1	
2	***CP SYSTEM DESIGN PROGRAMME - 3***
3	
4	***Phase 1 "Slice" tests complete (Milestone)***
5	
6	PB design iteration
7	***PB FDR/PRR (Milestone)***
8	PBs manufacture/assemble/test
9	***Tested PBs available***
10	
11	CMM design iteration
12	***CMM FDR/PRR (Milestone)***
13	CMMs manufacture/assemble/test
14	***Tested CMMs available***
15	
16	CPM design iteration
17	***CPM FDR/PRR (Milestone)***
18	CPMs manufacture/assemble/test
19	***Tested CPMs available***
20	
21	TCM specifications (Module-0)
22	***TCM PDR (Milestone)***
23	TCM pre-production design phase
24	***TCM FDR/PRR (Milestone)***
25	TCMs manufacture/assemble/test
26	***Tested TCMs available***
27	
28	P-ROD specifications (Module-0)
29	***P-ROD PDR (Milestone)***
30	P-ROD pre-production design phase
31	***P-ROD FDR/PRR (Milestone)***
32	P-RODs manufacture/assemble/test
33	***Tested P-RODs available***
34	
35	**Phase 2 "Slice" tests (Heidelberg) - 3 months**
36	
37	***Manufacture/assemble full CP system***
38	
39	***Full CP system available for system tests***
40