



Common Merger Module

⌘ **First Version of schematics finished and visible on Web:**

<http://www.te.rl.ac.uk/esdg/atlas-flt/specs/cmm/index.html>

⌘ **To be done:**

- some buffers need changing to cope with 5V tolerance issues relating to VirtexE devices**
- Connectors to CTP need to be decided upon.**

⌘ **New version of Schematic diagrams available Friday, hopefully.**

⌘ **Review required by Uli, Sam....**

⌘ **Ready for drawing office ~ 2 weeks**